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## GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER- IV EXAMINATION - SUMMER 2020

Subject	t Code: 2141001	Date:26/10/2020

## Subject Name: MICROPROCESSOR AND INTERFACING

Time: 10:30 AM TO 01:00 PM	Total Marks: 70
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## Instructions:

- 1. Attempt all questions.
- 2. Make suitable assumptions wherever necessary.
- 3. Figures to the right indicate full marks.

			MARKS
Q.1	(a)	Define: i) Instruction cycle ii) Machine cycle	03
	(b)	What are the control signals? How are they generated?	04
	(c)	Draw and Explain the internal block diagram of 8085 microprocessor.	07
Q.2	(a)	Explain DAD and DAA instruction with example.	03
	(b)		04
	(c)	Six bytes of data is stored in the memory locations from 3055h to	07
		305Ah. Transfer the data to the locations 3080h to 3085h in the reverse order.	
		OR	
	(c)	Ten bytes of data are stored in memory location between 2050H to	07
		2059H.Transfer the block of data to new memory location starting at 2070h.	
Q.3	(a)	Explain the following instructions with example.	03
	()	(1) LDA 2050h (2) XRI 40h (3) STAX B	
	(b)	Write a short note on flags in 8085 resister.	04
	(c)	Design a memory system that contains 2K byte of EPROM,	07
		immediately followed by 1K byte of RWM. The EPROM starts at	
		address 0000H and it is implemented by using 1Kbyte of EPROM. The	
		RWM is implemented using 1Kbyte RAM chips. Use decoder and gates	
		(if required) for the interfacing circuit.	
		OR	
Q.3	(a)	Discuss in detail and compare memory mapped I/O and I/O mapped	03
		I/O.	
	(b)		04
	(c) Define addressing modes? Explain the various addressing modes		07
		supported by 8085 microprocessor with example.	
Q.4			03
		i)ALE ii)INTR iii)HOLD	
	(b)	•	04
	(c)	Write a program to generate a square wave with period of 400µs. Use	07
		bit D0 to output the square wave. Assume the clock frequency 2MHz.	
		OR	
Q.4	(a)	What is conditional and unconditional branching? Illustrate the answer	03
		with an example.	
	(b)		04
		microprocessor? Write interrupt vector table for vectored interrupts.	
	(c)	Write a program to count continuously in Hexadecimal from FFh to 00H	07
		in a system with a 0.5µs Clock period. Use register C to set up a one	



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Firstranker's shoiced delay between First Ranker com Q.5 (a) List out features of 8086 microprocessor. 04 (b) Explain the mode-I input mode operation of 8255 in detail. (c) Draw and explain logic block diagram of 8279 (Programmable 07 Keyboard/Display IC). (a) Differentiate between 8085 and 8086 microprocessor. 0.5 0.3 (b) Explain the application of 8255. 04 (c) Draw and explain block diagram of Programmable Interrupt 07 Controller 8259.

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