

**GUJARAT TECHNOLOGICAL UNIVERSITY****BE- SEMESTER-VII (NEW) EXAMINATION – WINTER 2020****Subject Code:2172410****Date:28/01/2021****Subject Name:Power Electronics Design****Time:10:30 AM TO 12:30 PM****Total Marks: 56****Instructions:**

1. Attempt any FOUR questions out of EIGHT questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

		MARKS
<b>Q.1</b>	(a) Draw the structure & explain the working principle of Power BJT.	<b>03</b>
	(b) Enlist steps to design power electronics circuit.	<b>04</b>
	(c) Explain any one non-isolated driver circuit for SCR with design consideration.	<b>07</b>
<b>Q.2</b>	(a) Enlist & explain steps for Inductor design.	<b>03</b>
	(b) Discuss the design aspects and component selection for Snubber Circuit.	<b>04</b>
	(c) Discuss the effect of poor grounding for driver circuits.	<b>07</b>
<b>Q.3</b>	(a) Enlist the requirement of isolated driver circuit.	<b>03</b>
	(b) Write a technical note on di/dt and dv/dt protection of SCR.	<b>04</b>
	(c) Describe the steps involving in high frequency inductor design for boost converter.	<b>07</b>
<b>Q.4</b>	(a) Enlist the importance of isolated driver circuit.	<b>03</b>
	(b) Write a technical note on over current protection	<b>04</b>
	(c) Discuss the method to design high frequency transformer for Flyback type DC-DC converter.	<b>07</b>
<b>Q.5</b>	(a) Write a note on: opto-coupler.	<b>03</b>
	(b) Draw any one isolated driver circuit for MOSFET.	<b>04</b>
	(c) Discuss a method to measure analog quantity like voltage, current & frequency.	<b>07</b>
<b>Q.6</b>	(a) Discuss working of pulse transformer in brief.	<b>03</b>
	(b) Draw any one isolated driver circuit for IGBT.	<b>04</b>
	(c) Design non-isolated type driver circuit for TRIAC. Support your answer with suitable diagrams.	<b>07</b>
<b>Q.7</b>	(a) Define Heat Sink. List the types of Heat Sink according to heat Dissipation method.	<b>03</b>
	(b) Enlist & explain types of PCBs.	<b>04</b>
	(c) Write a short note on PCB design for mixed signal (analog and digital) circuit	<b>07</b>
<b>Q.8</b>	(a) Compare the Active and Passive Heat Sink.	<b>03</b>
	(b) Explain the Noise Reduction Through PCB Layout.	<b>04</b>
	(c) Write a short note on design considerations for Voltage Isolation and Current Capacity for PCB design.	<b>07</b>

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