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# B.Tech.(EIE) (2011 & Onwrads) (Sem.-3) ELECTRONICS DEVICES AND ANALOG ICs

Subject Code: EI-201 M.Code: 58007

Time: 3 Hrs. Max. Marks: 60

## INSTRUCTION TO CANDIDATES:

- SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
- SECTION-B contains FIVE questions carrying FIVE marks each and students have to attempt any FOUR questions.
- SECTION-C contains THREE questions carrying TEN marks each and students have to attempt any TWO questions.

### SECTION-A

# Write briefly :

- a. What is the use of DC load line?
- b. Draw the VI characteristics of the JFET?
- Write input resistance equation of hybrid equivalent CC configuration.
- d. What do you understand by heat sink?
- e. What is use of multistage amplifiers? List at least two advantage of it.
- Distinguish between couplings and bypass capacitor.
- g. Define the role of XY plate of CRO.
- h. What is "Dark current" of a photodiode?
- Explain the LC filter used in rectifiers.
- j. What is beam shaping?

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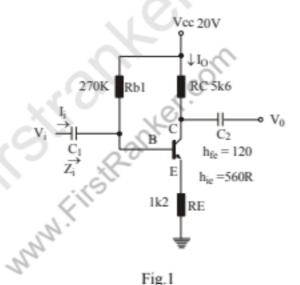


## SECTION-B

- Draw the hybrid model of a low frequency small signal voltage amplifier.
- Discuss the working of symmetric push pull amplifier with suitable diagram.
- What is JFET? Distinguish between FET and MOSFET.
- Explain the operation of N-channel MOSFET in depletion mode using suitable diagram.
- What is the basic principle of working of LCD's? How these differ from LED's.

#### SECTION-C

 In the circuit given below, calculate Z<sub>i</sub>, Z<sub>O</sub>, A<sub>V</sub>, and A<sub>i</sub>. Note that this CE stage uses a single bias resistor R<sub>BI</sub> which is the value R<sub>BB</sub>.



- What is the use of a rectifier? Compare and contrast half and full wave rectifiers.
- Write short notes on :
  - a. CRO
  - b. Transistor as switch

NOTE: Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.

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