

Roll No. Total No. of Pages: 02

Total No. of Questions: 18

B.Tech.(CSE)/(Electronics & Computer Engg.)/(IT) (2011 Onwards)

(Sem.-4)

MICROPROCESSOR & ASSEMBLY LANGUAGE PROGRAMMING

Subject Code: BTCS-404 M.Code: 56607

Time: 3 Hrs. Max. Marks: 60

INSTRUCTIONS TO CANDIDATES:

- 1. SECTION-A is COMPULSORY consisting of TEN questions carrying TWO marks each.
- 2. SECTION-B contains FIVE questions carrying FIVE marks each and students have to attempt any FOUR questions.
- 3. SECTION-C contains THREE questions carrying TEN marks each and students have to attempt any TWO questions.

SECTION-A SECTION-A NINFRIESTRATING N

Explain the following:

- 1. Interrupt
- 2. Data Bus
- 3. Flags
- 4. DAD Instruction
- 5. 3-byte Instruction
- 6. DMA
- 7. Use of 8251
- 8. PUSH Instruction
- 9. ANI
- 10. Latch Signal



SECTION-B

- 11. Write a note on Motorola 68000.
- 12. Describe serial and parallel data transfer techniques.
- 13. What is the use of DMA controller?
- 14. Differentiate between instruction, machine and clock cycle.
- 15. Write an assembly language program to swap two numbers.

SECTION-C

- 16. Explain the architecture and pin diagram of 8085.
- 17. Explain USART in detail.
- 18. Explain how traffic light system can be interfaced with 8085.

NOTE: Disclosure of Identity by writing Mobile No. or Making of passing request on any page of Answer Sheet will lead to UMC against the Student.

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