

[illegible]

**SECTION-B**

2. What is level translator circuit? Why is it used with cascaded differential amplifier?
3. What is the effect of variation in power supply voltages on offset voltage?
4. How does negative feedback affect the performance of an inverting amplifier?
5. Draw and explain circuit diagram of voltage-to-current converter. Also explain **any two** applications of this converter.
6. Discuss with circuit diagram operation of square wave generator.

**SECTION-C**

7.
  - a. Explain how 555 timer used as Schmitt trigger.
  - b. Draw and explain circuit diagram of an integrator circuit. Derive an expression for the output voltage.
8. What is the operating principle of PLL? Explain its applications.
9. Write a note on voltage regulators.

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