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## B.Sc. Part-II (Semester-III) Examination ELECTRONICS

(Electronics Devices and Circuits)

Tim	e : T	hree	Hou	irs]			[Maximum Marks:	80
Not	e :			estion No. 1 is compuls				
				w neat diagrams where	-			
1.	(A)	Fill	in th	ne blanks with correct v		2		
		(i)	In i	deal Op-Amp value of				
		(ii)	Bist	table multivibrator has				
		(iii)	D/A	is known as				
		(iv)	Volt	tage gain of Non-inverti				
	(B)	Cho	ose	the correct alternative :		2		
		(i)	In i	deal Op-Amp bandwidt				
			(a)	Zero	(b)	Minimum		
			(c)	Infinite	(d)	None		
		(ii)	The	monostable multivibra	tor has	stable state(s).		
			(a)	2	(b)	1		
			(c)	3	(d)	4		
		(iii)	Onc	of the following is no	t an oscillator	:		
			(a)	Colpitts	(b)	Wein bridge		
			(c)	Push pull	(d)	Hartley		
		(iv)	Op-	Amp IC 741 has total	pins.			
			(a)	2	(b)	6		
			(c)	14	(d)	8		
	(C)	Ans	wer	the following questions		4		
		(i)	Wh	at is feedback?				
		(ii)	List	the hybrid parameters.				
		(iii)	Def	ine CMRR.				
		(iv)	Wh	at is oscillator?				
	EITHER							
2.	(A)	Giv	e the	advantages and disadv	ect coupled am	plifier.	4	
		Draw hybrid equivalent circuit for CE transistor amplifier and						for
	, ,		(i) Current gain, (ii) Input impedance for CE-transistor amplifier. 8					
	OR							
	(P) Explain the working of single tuned amplifier with circuit diagram.							
(Q) State the advantages and disadvantages of RC coupled amplifier.								4

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3.	(A)	Explain the construction and working of Class B push pull amplifier. Find efficiency.	its 8				
	(B)	Explain cross over distortion. How it is eliminated ?	4				
	OR						
	(P)	Draw a circuit diagram of transformer coupled Class A amplifier and derive expressi	on				
		for its efficiency.	8				
	,	Give the classification of amplifiers.	4				
		THER					
4.		Explain Barkhausen criterion for sustained oscillations.	4				
		Explain the construction and working of Hartley oscillator.	8				
	OR						
	(P)	Explain the construction and operation of RC-phase shift oscillator using transist State its advantages.	tor. 8				
	(Q)	State the advantages of negative feedback.	4				
	EIT	THER					
5.	(A)	Explain the working of Op-Amp as non-inverting amplifier and derive the expressi for voltage gain.	ion 6				
	(B)	With suitable diagram explain the working of Op-Amp as summing amplifier.	6				
	OR						
	(P)	Explain the concept of virtual ground in Op-Amp.	4				
	(Q)	Define :	2				
		(i) Common mode voltage gain					
		(ii) Differential mode voltage gain.					
		Draw the block diagram of Op-Amp and explain the function of each block.	6				
	EIT	HER					
6.	(A)	Explain the construction and working of Op-Amp as a monostable multivibrator.	6				
		Explain how Op-Amp is used as damped harmonic oscillator.	6				
	OR						
	(P)	Explain the working of Op-Amp as a Schmitt Trigger.	6				
		Explain the working of Op-Amp as an astable multivibrator.	6				
_		HER					
7.		Explain the working of successive approximation type A/D converter.	8				
	(B)	Explain the terms :					
		(i) D/A Accuracy					
		(ii) D/A Resolution.	4				
	OR						
	(P)						
		Explain the need of D/A and A/D converter.	4				
	(R)	What is A/D and D/A converter ?	2				