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Dr. Babasaheb Ambedkar Technological University, Lonere

EXTC/Electrical/Computer/IT Department

Semester Examination

EX106

Subject: Basic Electronics Engineering (BETX) Date: 04 MAY 2017 Semester: I Instructions to students:

Max. Marks: 60 Time:-2.00 pm -5:00 pm

1. Attempt any five questions from question no. 1 to question no. 6. 2. Assume suitable data wherever necessary and mention it clearly.				
Q.	I A.	What is the need of Engineering materials? & classify them.	[8]	
	B.	Summarise Quantum numbers	[4]	
Q. 2	2 A.	Explain Drift current and diffusion current with neat diagrams	[6]	
	В.	A cylindrically shaped section of n-type silicon has a 1 mm length and 0.1		
		mm ² cross sectional area. Compute its conductivity and resistance	activity and resistance when it is	
purely intrinsic		purely intrinsic material. (The electron and hole density for intrin	a material. (The electron and hole density for intrinsic Si is	
		1.5×10^{10} /cm ³ Mobility constants for electrons and holes are 1500 cm ² /Vs respectively).	n ² /Vs and	
			[6]	
Q. 3	А.	Define Clipper and summarise different types of them.	[4]	
	В.	Illustrate Fixed Bias for transistor.	[4]	
	C.	Define PIV and describe Full wave Centre tapped Rectifier.	[4]	
Q. 4	A.	Explain various types of fixed resistors	[6]	
	В.	Find out the values of capacitors using colour coding		
·		(i) Brown, Black, Orange	[3]	
		(ii) Wide red, Yellow	[3]	
Q. 5	SOL	VE ANY TWO:		
	А.	Write a short note on Galvanometer	[6]	
	В.	Classify different types of transducers.	[6]	
	C.	Explain piezoelectric transducer.	[6]	
Q. 6	A.	Construct EX-OR gate by using NOR gates only	[4]	
	B.	Simplify $Y = \overline{ABC} + \overline{ABC} + \overline{ABC} + \overline{ABC} + \overline{ABC} + \overline{ABC}$	[4]	
	C.	(i) Convert $(1567.84)_{10} = (?)_{16}$	[2]	
		(ii) Convert Y=AB+AC+A into standard SOP form	[2]	

---All the Best-----

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