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Seat No.: _____ Enrolment No. GUJARAT TECHNOLOGICAL UNIVERSITY **BE - SEMESTER-I & II (NEW) EXAMINATION - WINTER 2019** Subject Code: 2110016 Date: 06/01/2020 **Subject Name: Basic Electronics** Time: 10:30 AM TO 01:00 PM Total Marks: 70 **Instructions:** 1. Question No. 1 is compulsory. Attempt any four out of remaining Six questions. 2. Make suitable assumptions wherever necessary. 3. Figures to the right indicate full marks. Mark **Q.1 Objective Question (MCQ)** 07 (a) Which are the logic gates known as a Universal Gates? 1. (a) XOR, AND, (b) AND, OR, (c) NAND, NOR, (d) XNOR, OR In most of modern IC op-amps, the 741 requires _____ power 2. supplies (a) 1 (b) 2 (c) 3 (d) 4 3. PAM stands for (a) Pulse And Modulation (b) Pulse Analog Modulation (c) Pulse Altitude Modulation (d) Pulse Amplitude Modulation Which is a typical application of digital signal processing? ' 4. (a) Noise elimination (b) Music signal processing (c) Image processing (d) All of the above The first cellular systems were _ 5. (b) Digital (c) Hybrid (a) Analog (d) None of Above Conductance is expressed in terms of 6. (a) ohm / m (b) m / ohm (c) mho / m(d) mho. A system is linear if ______ is true. 7. (a) KVL (b) KCL (c) Superposition theorem (d) Ohm's law **(b)** 07 Wireless mobile cells are ____ in shape 1. (a) pentagon (b) hexagon (c) circular (d) square Which of the following system has feedback network? 2. (a) Open loop (b) Closed loop Out of following signals ______ is an even signal. 3. (a) Cosine wave (b) Sine wave (c) Triangle wave (d) None of the above. A Flip Flop has got a memory of 4. (a) 1 bit (b) 2 bit (c) 4 bit (d) 8 bit In an Electrical system, the flow of current follows: 5. (a) De Morgan's law (b) Boyle's law (c) Curie's law (d) Ohm's law A radio station transmitting AM wave with 1 MHz frequency band 6. having a wavelength of _____ (a) 3 meter, (b) 300 meter, (c) 0.3 meter, (d) 30 meter 7. A circuit designed to increase the level of its input signal is called: (a) an amplifier (b) a modulator (c) an oscillator (d) a receiver

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Q.2	(a)	Draw the various rules to reduce/modification of Block Diagram system.	03
	(b) (c)	Write a short note on ammeter and voltmeter. Explain Kirchhoff's Voltage law with suitable example.	04 07
Q.3	(a) (b) (c)	Give the classification of electric network. State and explain Thevenin's theorem. Explain Node and Mesh Analysis with Controlled Sources., with example.	03 04 07
Q.4	(a) (b) (c)	Explain current to voltage convertor. Compare open loop and closed loop system. Explain ideal characteristics of ideal Op-Amp in detail.	03 04 07
Q.5	(a) (b) (c)	State and prove De-Morgan's theorems. Classify network topologies and draw each one of them. Give different types of flip flops. Explain any one of flip flop in detail.	03 04 07
Q.6	(a) (b) (c)	Draw only functional block diagram of signal processing system. What do you understand about multiplexing? Explain any one of the multiplexing technique. Explain in detail pulse modulation with necessary diagrams.	03 04 07
Q.7	(a) (b) (c)	Define waveguide, transmission lines and antenna. Classify the standard on 2G and 3G. Draw and explain the block diagram of superheterodyne AM radio receiver.	03 04 07