## GUJARAT TECHNOLOGICAL UNIVERSITY BE - SEMESTER-III (OId) EXAMINATION - WINTER 2019

Subject Code: 130901
Date: 28/11/2019
Subject Name: Circuits And Networks
Time: 02:30 PM TO 05:00 PM
Total Marks: 70

## Instructions:

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.
Q. 1 (a) State and explain Thevenin's and Norton's theorem with suitable examples.
(b) What are Y parameters and Z parameters? Derive the expression for Z parameters in terms of Y parameters and vice versa.
Q. 2 (a) Define following terms: (i) Linear and Nonlinear networks (ii) Lumped andDistributed Networks (iii) Passive and Active Networks (iv) Dependent Source
(b) State and explain initial value and final value theorem.

OR
(b) Obtain the laplce transform for $f_{1}(t)=t$ and $f_{2}(t)=t e^{-a t}$
Q. 3 (a) State and explain principle of duality.
(b) Solve for the nodal voltages $\mathrm{V} 1, \mathrm{~V} 2, \mathrm{~V} 3$ and V 4 as shown in the network in figure
, using nodal analysis.


## OR

Q. 3 (a) State the procedure to obtain solution of a network using laplace transform technique. State its advantages over classical method.
(b) In the network shown in fig. the switch K is opened at $\mathrm{t}=0$. find the valus of V , $\mathrm{dV} / \mathrm{dt}, \mathrm{d}^{2} \mathrm{~V} / \mathrm{dt}^{2}$ at $\mathrm{t}=0^{+}$if $\mathrm{I}=10 \mathrm{~A}, \mathrm{R}=10 \Omega$ and $\mathrm{L}=1 \mathrm{H}$

Q. 4 (a) State and explain maximum power transfer theorem. derive the condition for maximum power transfer to the load for d.c. circuits.
(b) In the network shown in fig. the switch K is moved from 1 to 2 position at $\mathrm{t}=0$,
steady state having previously been attained. Determine the current $\mathrm{i}(\mathrm{t})$

Q. 4 (a) What is meant by poles and zeros of a transfer function? what is significance of poles and zeros? Discuss the restrictions on locations of poles and zeros of transfer functions.
(b) Derive expression for rise of current and decay of current in RL series circuit excited by DC voltage source. Discuss the role of time constant in each.
Q. 5 (a) What are the procedure for formulation of graph, tree and incidence matrix? Hence discuss the procedure of forming reduced incidence matrix and its advantages.
(b) For the network shown in fig. draw the oriented graph.Also obtain incidence matrix (A), fundamental tie-set matrix (Bf) and fundamental cut-set matrix (Qf)


## OR

Q. 5 (a) Write and explain initial conditions for the inductor and capacitor at $t=0_{+}$and $t=\infty$
(b) Find the current passing through the 2 Ohm resistor using Mesh analysis for the circuit shown in the following figure.


