

Enrolment No._____

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BE - SEMESTER- V (New) EXAMINATION - WINTER 2019

Subje	ect (Code: 2150907 Date: 04/12/2	2019	
Subje	bject Name: Microprocessor and Microcontroller Architecture & Interfacing me: 10:30 AM TO 01:00 PM Total Marks: 70 tructions: 1. Attempt all questions. 2. Make suitable assumptions wherever necessary.			
Instru	ction	as:		
	3.	Figures to the right indicate full marks.		
Q.1	(a)	List four major differences between a microprocessor and microcontroller.	03	
Q.1	(b)	1	04	
	` ′			
	(c)	Explain the architecture of 8085 microprocessor with functional block diagram.	07	
Q.2	(a)	<u> </u>	03	
Q. <u>2</u>	(b)	• • • • • • • • • • • • • • • • • • • •	04	
	(0)	(2) CISC and RISC	04	
	(c)		07	
	(-)	8085 microprocessor with neat diagram.		
		OR		
	(c)	Explain editor, assembler, compiler and linker.	07	
Q.3	(a)	Explain TOMD in detail for 8051 microcontroller.	03	
	(b)	Draw and explain memory organization of 8051 microcontroller.	04	
	(c)	Draw and explain the functional block diagram of 8051 microcontroller.	07	
		OR		
Q.3	(a)	· ·	03	
	(b)	<u> </u>	04	
	(c)	1 , , ,	07	
0.4	()	(2) \overline{PSEN} (3)RXD & TXD (4) \overline{RD} & \overline{WR}	0.2	
Q.4	(a)		03	
	(b)	appropriate instruction. Explain POP and PUSH instructions w.r.t. 8051.	04	
	(b)	Explain POP and POSH instructions w.r.t. 8031. Explain various addressing modes of 8051 instructions with suitable	04 07	
	(c)	example	U/	
		OR		
Q.4	(a)		03	
ζ	(b)	*	04	
	(c)		07	
	` '	number system into hexadecimal number system.		
Q.5	(a)	Discuss the different types of interrupts available with 8051, state their	03	
		priorities.		
	(b)		04	
		2.5 KHz on P2.5. use timer 0 in mode 2 to create delay. Assume crystal		
		frequency =11.0592 MHz.		
	(c)		07	
		OR		
0.5	(6)	Write short note on "interfesing of A/D conventor with 9051 misses to 11-	ΛO	
Q.5	(a) (b)	_	03 04	
	(D)	8051 microcontroller.	V4	
	(c)		07	
		microcontroller.	٠,	
