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BE - SEMESTER-V (Old) EXAMINATION – WINTER 2019				
Subject Code: 151003 Date: 25/11/2019				
	•	Name: Integrated Circuits And Applications 0:30 AM TO 01:00 PM Total Marks: 70		
Instructions:				
	2.	Attempt all questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks.		
Q.1	(a) (b)	 (i) Output Offset voltage (ii) Input Offset voltage (iii) Input Bias Current (iv) Input Offset current (v) CMRR (vi) SVRR (vii) Slew Rate 	07 07	
	(0)	equation of Close Lop Gain Af and Input Impedance Rif.	07	
Q.2	(a)	packages of Op. Amp.	07	
	(b)	Explain Differential Amplifier using one Op. Amp. and derive the equation of differential gain A _D .	07	
	(b)	-	07	
Q.3	(a) (b)		07 07	
Q.3	(a)	Explain Voltage to current converter i. With floating load ii. With Grounded load	07	
	(b)	Explain Absolute value output circuit in detail.	07	
Q.4	(a)	draw practical differentiator circuit and frequency response.	07	
Q.4	(b) (a)	OR OR	07 07	
	(b)	necessary waveforms and equation.	07	
Q.5	(a)	Draw the circuit of first order Butter worth High Pass filter and explain it with necessary equation. Also draw it,s frequency response.	07	
	(b)		07	
Q.5	(a) (b)	Explain positive and negative Three Terminal Voltage Regulators.	07 07	

(b) Draw the circuit diagram of Astable multivibrator using IC 555 and explain it's working with necessary waveform and equations.

1