FirstRanker.com Enrolment FirstRanker.com www.FirstRanker.com **GUJARAT TECHNOLOGICAL UNIVERSITY BE - SEMESTER-IV(NEW) - EXAMINATION - SUMMER 2019** Subject Code:2141002 Date:09/05/2019 Subject Name: Analog Circuit Design Time:02:30 PM TO 05:00 PM **Total Marks: 70 Instructions:** 1. Attempt all questions. 2. Make suitable assumptions wherever necessary. 3. Figures to the right indicate full marks. MARKS (a) List out characteristics of ideal op-amp. 03 **Q.1** (b) Compare RC phase shift oscillator with Wien bridge oscillator. 04 (c) List the parameter those affecting to the transistor at high frequencies. Draw the 07 hybrid π model for CE configuration and explain it. Also derived the equation for any two above listed parameter for CE configuration. Q.2 Write short note on validity of hybrid- π model. 03 (a) Draw and explain the equivalent circuit of a crystal. 04 **(b)** (c) Explain various open loop configuration of op-amp. 07 OR (c) Derive expression for closed loop gain of a voltage series feedback amplifier 07 using op-amp. **Q.3** Draw and explain block diagram of op-amp. 03 (a) Define the following parameters of Op-Amp: (i) Input bias current (ii) CMRR 04 **(b)** (iii) Input offset voltage (iv) Input offset current Show how Op-Amp can be used as summing, scaling and averaging amplifiers (c) 07 using non inverting configuration. OR What is differential amplifier? Explain in brief. Q.3 (a) 03 (b) Define the following parameters of Op-Amp: (i) Slew Rate (ii) PSRR (iii) Gain-04 Bandwidth Product (iv) Output offset voltage Explain the basic differentiator using an op-amp. What are the problems (c) 07 associated with this configuration? How they are overcome? State the properties of Butterworth filter. 03 **Q.4 (a)** Draw and explain zero crossing detector. 04 **(b)** Draw the non-inverting Schmitt trigger circuit and explain the threshold levels 07 (c) and hysteresis. OR What is thermal drift? How does it affect the performance of an op-amp circuit? **Q.4** 03 **(a)** Draw and explain peak detector. 04 **(b)** What is PLL? Explain operation of PLL with basic blocks and mention any four 07

- (c) What is PLL? Explain operation of PLL with basic blocks and mention any four applications of it in radio communication.
 Q.5 (a) Briefly explain Notch filter.
 (b) For an astable multivibretor using 555 timer, R_A=2.2KΩ, R_B= 3.9KΩ and C=0.1
 - (b) For an astable multivibretor using 555 timer, $R_A=2.2K\Omega$, $R_B=3.9K\Omega$ and C=0.1 µF, determine the positive pulse width, negative pulse width and free running frequency.

(c) Draw sallen-key LPF and derive its transfer function.

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Q.5	(a)	What is the difference between active and passive filter.	03
	(b)	Discuss magnitude and frequency scaling in filter design.	04
	(c)	Show how Bi-quad circuit can be used as a universal filter?	07

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