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	BE - SEMESTER-V (NEW) EXAMINATION – SUMMER 2019		
Subject Code: 2150907 Date		31/05/2019	
iect N	Name: Microprocessor and Microcontroller Architecture & Interfa-	cing	
		1100 70	
3.	Figures to the right indicate full marks.		
		MARKS	
(a)	Explain all the bits of PSW register.	03	
(b)	What is the function of following pins in 8085:	04	
	TRAP & HLDA		
(c)		07	
	instruction for 8085.		
(a)	Differentiate between MOVX & MOVC instructions	03	
	How stacks are accessed in 8051? Explain PUSH & POP instructions.	04	
(c)	How demultiplexing of address/data lines (AD0 – AD7) can be	07	
(0)	1 0	0.	
(a)		07	
(0)	write a short note on interrupts available in 8031.	07	
(a)	Explain assembly directives.	03	
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()		0.2	
		03	
	1 1 2 2	04	
(C)	write an assembly program to add two to bit numbers.	07	
(a)	What is the function of temporary registers W & Z in 8085	03	
(b)	Compare Von Neumann & Harvard architecture.	04	
	(a) (b) (c) (a) (b) (c) (a) (b) (c) (a) (a) (b) (c) (b) (c) (c) (a) (b) (c) (c) (a) (b) (c) (c) (c) (c) (c) (c) (c) (c) (c) (c	ject Code: 2150907 ject Name: Microprocessor and Microcontroller Architecture & Interface: 02:30 PM TO 05:00 PM Total Manuctions: 1. Attempt all questions. 2. Make suitable assumptions wherever necessary. 3. Figures to the right indicate full marks. (a) Explain all the bits of PSW register. (b) What is the function of following pins in 8085: TRAP & HLDA (c) Draw & explain the timing diagram of MVI A, 32H instruction for 8085. (a) Differentiate between MOVX & MOVC instructions (b) How stacks are accessed in 8051? Explain PUSH & POP instructions. (c) How demultiplexing of address/data lines (ADO – AD7) can be achieved? Also explain the generation of all the control signals. OR (c) Write a short note on interrupts available in 8051. (a) Explain assembly directives. (b) Explain the instruction DJNZ & JBC in 8051 (c) Write an assembly program to find 0's & 1's from a given number. OR (a) List all the data types available in C. (b) Explain the programming model of 8085. (c) Write an assembly program to add two 16 bit numbers.	

ms.

(c)

OR Why C programming is preferred over assembly programming? 03 **Q.4** (a) Find control word to be loaded in timer registers THO & TLO to 04 **(b)** generate time delay of 25 ms. Assume clock frequency as 11.0592MHz Write a C program to continuously transfer the message "YES" serially **07** (c) at 9600 baud, 8 bit data, and 1 stop bit. **Q.5** 03 (a) What is RETI instruction? How it is different from RET instruction? List the alternate functions of all the pins of port 3. 04 **(b)** Draw & explain the interfacing of matrix keyboard with 8051. **07** (c)

Write a C program to toggle all the bits of port 1 at a time delay of 50

OR

Q.5 Explain the advantage of IDE in program development. 03 (a) Write a short note on types of memory. 04 **(b)** Explain the role of SBUF & SCON registers in serial transfer in 8051. **07** (c)

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