

## **GUJARAT TECHNOLOGICAL UNIVERSITY**

**BE - SEMESTER-VII(NEW) EXAMINATION - SUMMER 2019** 

Subject Code:2171711			Date:18/05/2019	
Ti	•	Name:Embedded System Design 2:30 PM TO 05:00 PM Total Marks: 70		
1110	1. 2. 3.	Attempt all questions.  Make suitable assumptions wherever necessary.		
Q.1	(a)	Explain differences between embedded systems and general purpose computing systems	03	
	(b) (c)	Explain difference between CISC and RICS architecture.  Explain block diagram of embedded system in detail with neat diagram	04 07	
Q.2	(a) (b) (c)	Compare process, task and thread.  Explain function of CPSR register of ARM processor.  Explain in detail the ARM programmers model in detail with neat sketch  OR	03 04 07	
	(c)	Explain pipeline concept of ARM processor.	07	
Q.3	(a)	Explain difference between ADDS and ADD instructions in ARM Assembly language.	03	
	(b) (c)	Explain LDMIA instruction in detail with all possible operands. Write a program to find average of 10 numbers stored in an array. Consider data size of 32 bits	04 07	
Q.3	(a) (b) (c)	OR Enlist all the control flow instructions of ARM assembly language. Explain all the multiplication instructions of ARM assembly language in detail. Write a program in ARM assembly language to find largest number from an array. Assume the array length and starting address suitably.	03 04 07	
Q.4	(a) (b) (c)	What is meant by RTOs. How they differ from other operating systems. Explain instructions: STMIB and BICS Write a program in embedded C to blink LEDs connected to alternate bit of port0 i.e. P0.0, P0.2, P0.4 and so on up to P0.30 of ARM GPIO.	03 04 07	
Q.4	(a) (b) (c)	OR Enlist various serial communication protocols. Explain Watchdog Timer in brief. Write a program in ARM embedded C to blink LED connected to Port pin P0.10 with 75 % duty cycle and 10 ms OFF time period.	03 04 07	
Q.5	(a) (b) (c)	State advantages and disadvantages of I <sup>2</sup> C protocol Explain how PWM works for DC loads Explain memory management in RTOs in detail.  OR	03 04 07	
Q.5	(a)	Write short note on "Harvard Architecture"	03	
	(b) (c)	Write a brief note on exception handling in ARM processor.  Explain USB protocol in detail.	04 07	

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