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GUJARAT TECHNOLOGICAL UNIVERSITY

GUJARAT TECHNOLOGICAL UNIVERSITY			
BE - SEMESTER-VII(NEW) EXAMINATION – SUMMER 2019 Subject Code: 2172408 Date:16/05/2019 Subject Name: A dwanged Derver Electropics Devices & Interface Circuits			
Inflet.02.30 FWI TO 03.00 FWI TO 10141 Warks. 70 Instructions:			
1. Attempt all questions.			
2. Make suitable assumptions wherever necessary.			
	3. F	igures to the right indicate full marks.	
Q.1	(a)	State applications of WBG materials.	03
V .1	(b)	••	04
	(c)	Explain the any Non-Isolated Driver ICs used for Half Bridge.	07
Q.2	(a)	State different methods of galvanic isolation.	03
	(b)	1	04
	(c)	Explain the measurement & interfacing of analog signals with Digital	07
		System. OR	
	(c)	Explain any DC Current measurement techniques.	07
	(0)		01
Q.3	(a)	Draw schematic diagram of isolated linear amplifier.	03
	(b)	1 1	04
	(c)	Write technical note on differential voltage probe.	07
01	(-)	OR Evaluin the importance of Crown diag for Dewar Circuits	02
Q.3	(a) (b)	Explain the importance of Grounding for Power Circuits. Explain the F to V Converter.	03 04
	(b) (c)	Explain the Interfacing of Signals Using Galvanic Isolation.	04
	(C)	Explain the interfacing of Signals compoundance isolation.	07
Q.4	(a)	Explain the Power Scope.	03
	(b)		04
	(c)	Explain how a TTL IC can be interfaced with CMOS IC. Draw	07
		interconnection diagrams also.	
04	(a)	OR State verious principles used in transducers used for current	03
Q.4	(a)	State various principles used in transducers used for current measurement in industry.	03
	(b)	What is logic analyzer? State its applications.	04
	(c)	Explain the Interfacing of Digital Signals with Different Voltage	07
		Levels.	
0.5			0.2
Q.5	(a) (b)	State various logic families.	03
	(b) (c)	Explain the Signal Conditioning. It is required to interface a logic signal of 0-24 V with microprocessor.	04 07
	(\mathbf{C})	With neat diagram explain interface circuit required for this.	07
		OR	
Q.5	(a)	What is meaning of VOH and VOL voltage levels of logic ICs.	03
-	(b)	Write the Merits and Demerits of WBG materials.	04
	(c)	Write technical note on current probe for power electronics.	07
