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Sea	t No.:	Enrolment No.	
Su	bject	GUJARAT TECHNOLOGICAL UNIVERSITY BE - SEMESTER–VII (OLD) EXAMINATION – SUMMER 2019 Code: 172403 Date: 16/05/2019 Name:Power Processing Circuits - II	
	tructio 1.	Attempt all questions. Make suitable assumptions wherever necessary.	U
Q.1	(a) (b)	Describe operation of Mc murray –Bedford inverter. Discuss the different PWM technique.	07 07
Q.2	(a) (b)	Draw and explain basic series inverter. What are its limitations? Explain the space vector modulation technique for the control of an inverter output.	07 07
	<b>(b)</b>	<b>OR</b> Why multi-level inverter is required? Describe basic operation of same.	07
Q.3	<b>(a)</b>	Explain 120° mode operation of three-phase inverter with circuit diagram and waveforms.	07
	<b>(b</b> )	Compare ZVS and ZCS.	07
Q.3	(a)	OR Explain 180° mode operation of three-phase inverter with circuit diagram and	07
	(b)	waveforms. Explain the Fourier analysis of a square wave inverter and thus derive the THD in the output voltage waveform.	07
Q.4	(a) (b)	Explain the external control of ac output voltage of an inverter. Draw the block diagram of on-line UPS. Explain each block in brief. OR	07 07
Q.4	(a) (b)	What are the transformer tap changers? Draw the block diagram of off-line UPS. Explain each block in brief.	07 07
Q.5	(a) (b)	Write short note on:- Active front-end rectifier. Explain the single-phase bidirectional controller having RL load with neat waveforms.	07 07
Q.5	(a) (b)	<b>OR</b> Discuss Triplen harmonic injection technique for inverter control. Explain cascaded H – bridge multi-level inverter in brief.	07 07

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