t<mark>Ranker.com</mark> Enrolment No. GUJARAT TEGHENGHAGAGAL UNIVERSISTAnker.com **BE - SEMESTER-VIII (OLD) EXAMINATION - SUMMER 2019** Subject Code: 181104 Date: 09/05/2019 Subject Name: Advanced Microprocessors Time: 10:30 AM TO 01:00 PM **Total Marks: 70 Instructions:** 1. Attempt all questions. Make suitable assumptions wherever necessary. 2. 3. Figures to the right indicate full marks. (a) Explain the difference between 8086 features and its internal **Q.1** 07 architecture. (b) Draw and explain 8086 pin diagram. 07 Q.2 Explain 8086 Memory Interfacing and the signals used in it. Show 07 (a) with schematic Explain how the two memory banks are separated? (b) Explain Base plus index addressing mode in 8086 microprocessor 07 OR (b) Explain in brief: I/O interfacing in 8086 07 Write an assembly language program using 8086 instruction to Add block of 0.3 07 (a) ten numbers from one location and store to another memory location (b) Write an assembly language program using 8086 instruction to arrange an 07 array of five numbers in ascending order. OR Explain following 8086 instructions with an example. (i) XLAT (ii) LOOPZ 07 Q.3 (a) (iii) LEA (iv) STOSB (b) Explain the special purpose registers of 8086 microprocessor 07 (a) Describe the functions of following 8086 pins. (i) HLDA (ii) NMI (iii) Q.4 07 **READY** (iv) CLK (b) Explain how the OUT DX,AX instruction operates. 07 OR 0.4 Explain stack memory addressing mode in 8086 microprocessor. 07 (a) (b) Describe the operation of the IMUL BX,DX,100H instruction. 07 0.5 (a) Explain the multipurpose registers of 8086 microprocessor 07 (b) List the important features of 80186 micro-processor. Explain the function of 07 DMA unit of 80186 microprocessor. OR Explain Pentium-IV Processors in brief and discuss advancements as 07 **Q.5** (a) compared to previous processors in x86 Family. (b) Briefly describe branching and fusion mechanism in Core 2 Duo processor. 07 \*\*\*\*\*