

# GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER-IV (NEW) EXAMINATION – WINTER 2018

**Subject Code:2142405**

**Date:28/11/2018**

**Subject Name:Analog Electronics and Its Applications**

**Time: 02:30 PM TO 05:00 PM**

**Total Marks: 70**

**Instructions:**

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

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|------------|---|-----------|
| <b>Q.1</b> | (a) Define followings.<br>(1) PIV (2) Q- point (3) Noise Figure.  | <b>03</b> |
|            | (b) Define the following.<br>(i) CMRR (ii) PSRR (iii) Slew Rate (iii) Noise   | <b>04</b> |
|            | (c) Draw an IC OP Amp in block diagram form. Identify each stage by function.   | <b>07</b> |
| <b>Q.2</b> | (a) Draw Equivalent Circuit of OP-amp.  | <b>03</b> |
|            | (b) Enlist Ideal Op-Amp characteristics.  | <b>04</b> |
|            | (c) Compare CE, CB, and CC configuration in tabular form.   | <b>07</b> |
|            | <b>OR</b>   |           |
|            | (c) Draw & Explain Full wave Bridge Rectifier circuit. Also derive expression for output frequency.   | <b>07</b> |
| <b>Q.3</b> | (a) State the advantages of TTL.  | <b>03</b> |
|            | (b) Explain the construction of AND logic gates using discrete components like diode, resistor, Transistor etc.   | <b>04</b> |
|            | (c) Explain working of successive approximation ADC with suitable numerical example.  | <b>07</b> |
|            | <b>OR</b>   |           |
| <b>Q.3</b> | (a) Explain in brief Zero Crossing Detector.  | <b>03</b> |
|            | (b) What is need of Schmitt trigger circuit? Enlist & explain comparator characteristics.   | <b>04</b> |
|            | (c) Compare DTL, TTL & RTL in tabular forms.  | <b>07</b> |
| <b>Q.4</b> | (a) Explain in brief block diagram of linear regulated power supplies.  | <b>03</b> |
|            | (b) Write a technical note on: Negative Impedance converter.  | <b>04</b> |
|            | (c) Explain bistable multivibrator using 555. Give necessary equations and draw neat figures and waveforms.   | <b>07</b> |
|            | <b>OR</b>   |           |
| <b>Q.4</b> | (a) Explain 78xx and 79xx voltage regulators.   | <b>03</b> |
|            | (b) Compare AC and DC equivalent circuit.   | <b>04</b> |
|            | (c) Explain with neat sketches F/V converter.   | <b>07</b> |
| <b>Q.5</b> | (a) A single stage transistor amplifier has a voltage gain of 600 without feedback and 50 with feedback, calculate the percentage of output which is feedback to input. | <b>03</b> |
|            | (b) Explain Switched Capacitor Filter circuit in detail.  | <b>04</b> |
|            | (c) Draw and Explain single stage CE amplifier circuit. What is role of CE, Cc, and Cin in single stage CE amplifier circuit?   | <b>07</b> |

- Q.5**
- (a) What is Barkhausen criterion? The tuned collector oscillator makes use of an L-C tuned circuit with  $L = 29.3\mu\text{H}$  and  $C = 450\text{pF}$ . determine frequency of oscillation. **03**
  - (b) List out the advantages of negative feedback. **04**
  - (c) Explain Summing, Scaling and Averaging Amplifier circuit with necessary equation and neat sketches. **07**

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