

www.FirstRanker.com

Enrolment.FirstRanker.com

GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER-V (NEW) EXAMINATION – WINTER 2018			
Sub	ject	Code:2153901 Date:27/1	11/2018
Sub	ject	Name:Fabrication of Nano- devices	
Time: 10:30 AM TO 01:00 PM Total Man			arks: 70
Instructions:			
	1.	Attempt all questions.	
	2.	Make suitable assumptions wherever necessary.	
	3.	Figures to the right indicate full marks.	
			MARKS
Q.1	(a)	Define Source, Drain, Gate and Channel in MOSFET.	03
	(b)	Describe Quantum wells, wires, and dots. Its size and dimensionality effects.	04
	(c)	Explain Single Electron Tunneling with proper circuit diagram, Quantum conditions and Coulomb Staircase.	07
Q.2	(a)	What is CMOS explain briefly	03
	(u) (h)	Explain Core-Shell Structures	04
	(c) (c)	Describe Excitons and its types. Briefly explain the concept of binding	07
	(0)	energy and its effect on special extension.	01
		OR	
	(c)	Describe Graphene.	07
Q.3	(a)	Define Briefly different types of Lithography.	03
	(b)	Explain DNA based Biosensors.	04
	(c)	Write a short note on Protein Based Biosensors.	07
		OR CO	
Q.3	(a)	Define Semiconductor Quantum Dots.	03
	(b)	What are Tunnel Diodes and Explain Resonant Tunneling Transistor	04
	(c)	Explain how Nanosensors are used in Sensing various gases.	07
Q.4	(a)	Define Gas Sensitivity.	03
	(b)	Explain Nano Robotics and Nanomanipulation.	04
	(c)	Write shot note on MOSFET (Components, Working Application with	07
		Diagrams)	
04	(a)	Define Magnetic Nenonerticles and its Applications	02
Q.4	(a) (b)	Define Photonic Crystals and its types	03
	(\mathbf{D})	Define Schottky Contact Schottky Barrier Diode and hence list	04
	(C)	Applications of Schottky Device	07
Q.5	(a)	Write down Applications of Single Electron Tunneling	03
	(u) (h)	Explain Atom lithography	04
	(\mathbf{c})	Explain $\mathbf{Y}_{\mathbf{P}}$ av Lithography	07
			07
Q.5	(a)	List out Applications of Photonic Band Gap Devices	03
	(h)	Describe Graphene.	04
	(c)	Describe Single Electron Devices and Single Electron Dynamics.	07