

GUJARAT TECHNOLOGICAL UNIVERSITY**BE - SEMESTER-V (OLD) EXAMINATION – WINTER 2018****Subject Code:150701****Date: 30/11/2018****Subject Name: Advance Processors****Time: 10:30 AM TO 01:00 PM****Total Marks: 70****Instructions:**

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

- Q.1** (a) Draw and explain 8086 Architecture block diagram. **07**
(b) Explain the addressing modes of 8086 with example. **07**

- Q.2** (a) Explain following 8086 instructions. **07**
1. MUL 2. LOOP 3. MOVSB 4. ADD
(b) Write an assembly language program to find the factorial of the given no. **07**

OR

- (b) Write an assembly language program to move data from one location to another. **07**

- Q.3** (a) **07**
Define Segmentation. Advantages of Segmentation. List out four segments of 8086 with its purpose.

- (b) Discuss Minimum mode operation of 8086 . **07**

OR

- Q.3** (a) Explain the parameter passing methods with example. **07**
(b) Describe Reentrant and Recursive procedures **07**

- Q.4** (a) Write a brief note on Virtual 8086 Mode **07**
(b) . Explain Pentium Processor Architecture **07**

OR

- Q.4** (a) Explain MMX and hyper threading **07**
(b) Describe the Access Right Byte in 80286 processor. **07**

- Q.5** (a) Explain the concept of memory banks in 8086 . **07**
(b) Explain the merits and demerits of RISC and CISC CPU. **07**

OR

- Q.5** (a) Describe the architecture of SUN SPARC processor. **07**
(b) Describe the architectural features of Core-2 duo processor. **07**
