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GUJARAT TECHNOLOGICAL UNIVERSITY BE - SEMESTER-VII (NEW) EXAMINATION - WINTER 2018 Subject Code: 2172001 Date: 15/11/2018 **Subject Name: Microcontrollers and Embedded Systems** Time: 10:30 AM TO 01:00 PM **Total Marks: 70 Instructions:** 1. Attempt all questions. 2. Make suitable assumptions wherever necessary. 3. Figures to the right indicate full marks. MARKS What is embedded software? What is the purpose of a Q.1 (a) 03 watchdog timer in embedded systems? Differentiate between RISC & CISC architecture. **(b)** 04 (c) Design any micro-controller based application & explain with 07 the help of a neat block diagram. Q.2 Write the steps to program Timer 1 in mode 2. 03 **(a) (b)** WAP in C to count the no. of times an interrupt is generated 04 upon pressing of a push button connected to INTO and display it on P2 port. (c) Draw an interfacing diagram of DC motor with 8051. WAP to 07 generate 66% PWM signal to run the motor. OR Interface a 3x2 matrix keyboard motor with 8051. (c) 07 Q.3 What is the significance of RI & TI flags? 03 **(a)** Explain the bit configuration of TCON register. **(b)** 04 WAP in C to serially transmit the string "NEXUS" at 56kbps (c) 07 baud. OR Define step angle & Holding torque. Give relation between Q.3 03 (a) steps per second & rpm of motor. Draw & explain 8051 connections to RS-232 port using **(b)** 04 MAX232 IC. WAP in C to count pulses fed externally on T0 pin & display 07 (c) on P1 & P2 ports. Draw the programming model of PiC18f4xx. **Q.4 (a)** 03 State & explain instructions related to subroutines. **(b)** 04 Draw & explain the memory organization in PiC18F4xx. (c) 07 OR **Q.4** Explain current sourcing with example. 03 **(a)** State & explain any 4 machine control instructions. **(b)** 04 Explain PUSH & POP instructions with a programming 07 (c) example. Q.5 Draw internal structure of PORTB. 03 **(a)** Explain bit configuration of INTCON SFR. 04 **(b)** Write an Assembly language program to find an average of 8 07 (c) numbers stored at address starting from 0x120 & save the answer. OR



- Explain bit configuration of registers T1CON SFR. **(b)**
- Write an Assembly language program to transfer a block of 10 07 (c) data bytes from program memory address 0x010110 to data memory addresses starting from 0x100.

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