

## **GUJARAT TECHNOLOGICAL UNIVERSITY**

**BE - SEMESTER-VII (NEW) EXAMINATION - WINTER 2018** 

Subject Code: 2172408	Date: 26/11/2018
<b>Subject Name: Advanced Power Electronics Devices</b>	s & Interface Circuits
Time: 10:30 AM TO 01:00 PM	Total Marks: 70
Instructions:	
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1. Attempt all questions.

- 2. Make suitable assumptions wherever necessary.
- 3. Figures to the right indicate full marks.

Q.1	(a) (b) (c)	Why do semiconductors have a band gap? State various wide band gap materials and their energy band gap. Compare WBG (Sic or GaN) with Silicon based power electronics	03 04 07
Q.2	(a)	Discuss need of MOSFET driver? Enlist isolated and non-isolated driver ICs.	03 04
	(b) (c)	Draw functional diagram of ULN2803 IC. State advantages of this IC over relay driver using discrete components.  OR	07
	(c)	Discuss use of MOC3020 to control speed of fan.	07
Q.3 (a)		Write short note on Current Transformer.	03
	<b>(b)</b>	Discuss Gate Drive Requirements Of High-Side Devices.	04
	<b>(c)</b>	Discuss F to V Converter.	<b>07</b>
		OR	
Q.3 (	(a)	State working principle of Frequency & Speed Measurement.	03
	<b>(b)</b>	Explain application of Linear Opto-coupler.	04
	<b>(c)</b>	Compare Logic Analyzer with DSO.	<b>07</b>
<b>Q.4</b>	(a)	Explain requirement of Isolation Amplifier.	03
	<b>(b)</b>	Write short note on Measuring and Interfacing Analog Signals.	04
	<b>(c)</b>	Explain the measurement & interfacing of analog signals with Digital	<b>07</b>
		System.	
		OR	
Q.4 (a) (b)		Draw Analog to Digital converter circuit.	03 04
		Necessarily Better?	۰.
	<b>(c)</b>	Explain Voltage Measurement using Discrete Components.	07
Q.5	(a)	Draw schematic diagram of CRO.	03
Ų.S	(a) (b)	Compare CRO with DSO.	03
	(c)	Explain how a TTL IC can be interfaced with CMOS IC. Draw	07
	(C)	interconnection diagrams also.	U/
		OR	
Q.5	(a)	Draw block diagram of CRO.	03
V.2	(a) (b)	Write short note on Grounding for Power Circuits.	03
	(c)	Write short note on use of Power Scope for power electronics	07
	(C)	applications.	U/

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