DR. BABASAHEB AMBEDKAR TECHNOLOGICAL UNIVERSITY, LONERE

Mid Semester Examination – Oct 2019

Course: B. Techin CSE | **Sem: III**

Subject Name: Computer Architecture & Organization | **Subject Code:BTCOC304**

Max Marks:20 | **Date:-07/10/2019** | **Duration:- 1 Hr.**

Instructions to the Students:

1. Draw the neat diagram whenever it is necessary

Q1	Fill in the Blanks	Course Outcome	BT Level	6 Marks
	1. 2's Complement representation of -10 is a. 11110110 b. 11011001 c. 00001010 d. 11111100	CO1,CO2, CO3	1,2	
	2. …….. is not a status flag bit a. I flag bit b. P flag bit c. Z flag bit d. S flag bit			
	3. ……..converts the program written in assembly language into machine language a. Machine Compiler b. Interpreter c. Assembler d. Converter			
	4. Range of E' for normal value in single precision is …… a. 0 <E'<255 b. -127 < E' < 127 c. -1022 <= E'<= 2047 d. None of the above			
	5. Recoded multiplier of 101100 as per Booths multiplication is… a. -1 +1 0 -1 0 0 b. 1 0 -1 0 +1 -1 c. Both a and b d. None of above			
	6. In single precision format ……bits for sign ……..bits for exponent and …….bits for mantissa a. 1,7,22 b. 1,8,23 c. 1,9,23 d. None of above			

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Q.2	**Solve Any Two of the following.**			**6 Marks**
(A)	Draw and Explain System bus Model of Computer System	CO2,CO3	2,3	
(B)	Represent the -307.0625 in single and double precision format .			
(C)	Perform the multiplication of 5*4 using un-sign multiplication algorithm			
Q.3	**Solve Any One of the following.**			**8 Marks**
(A)	Differentiate between Restoring and Non-restoring division algorithm and perform the division using both the algorithm by considering Dividend=1011 and Divisor=0101	CO3	2,3	
(B)	Solve the 5 * 4 multiplication using Booths algorithm for Sign multiplication and Booth's recoded multiplier algorithm.			

***** All the Best *****

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