Firstranker's choice www.FirstRanker.com www.FirstRanker.com = ! \oplus What is interrupt? what are different types of interrupts? Explain

co(4)

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Mid Semester Examination – March - 2019

Q.No.1 Fill in the blanks (06)	a. Masking b. Unmasking c. both a and b d. none of these 5. how many bit data bus 8085 is a. 8 bit b. 16 bit c. 32 bit d. 64 bit 6. ALE pin of 8085 is a. Address latch enable b. arithmetic logic enable c. address logic enable d. none of these.	A.) Explain addressing modes with example in detail B.) Subtract two 8 bit numbers available in reg B and reg C, result will be stored in 2201H memory location	Q.No.3 Solve any one of the following (A) Write an assembly language program to find largest number from series of numbers available in memory location from 2301H co(2) on words, store result in 2400H.
	ANT instruction is used to	Instruction is used to fasking b. Unmasking c. both a and b d. none of these w many bit data bus 8085 is bit b. 16 bit c. 32 bit d. 64 bit pin of 8085 is ddress latch enable b. arithmetic logic enable c. address logic enable d. none of these. Solve any two of the following: (3x2)	a. Masking b. Unmasking c. both a and b d. none of these how many bit data bus 8085 is a. 8 bit b. 16 bit c. 32 bit d. 64 bit ALE pin of 8085 is a. Address latch enable b. arithmetic logic enable c. address logic enable d. none of these. 9. Solve any two of the following: A.) Explain addressing modes with example in detail B.) Subtract two 8 bit numbers available in reg B and reg C, result will be stored in 2201H memory location C.) Write an instructions to i. load 00H in accumulator ii. Decrament accumulator iii. Move content of accumulator to Reg B
much memory can be accessed by 8085 much memory can be accessed by 8085 k byte b. 1 k byte c. 3 k byte d. none of these V A,B instruction is used to transfer data from to A b. A to B c. A to A d. none of these I instruction is used to fasking b. Unmasking c. both a and b d. none of these w many bit data bus 8085 is bit b. 16 bit c. 32 bit d. 64 bit pin of 8085 is ddress latch enable b. arithmetic logic enable c. address logic enable d. none of these. Solve any two of the following: A.) Explain addressing modes with example in detail B.) Subtract two 8 bit numbers available in reg B and reg C, result will be stored in 2201H memory location	Solve any two of the following: A.) Explain addressing modes with example in detail B.) Subtract two 8 bit numbers available in reg B and reg C, result will be stored in 2201H memory location		