

84-IT

DR. BABASAHEB AMBEDKAR TECHNOLOGICAL UNIVERSITY, LONERE Mid Semester Examination – March 2019 Course: B. Tech in Information Technology Sem: IV Duration:- 1 Hr. Subject Name: MPMC Microprocessor & Microcontroller Subject Code: BTITTC401 Max Marks: 20 Date:-11/03/2019			
Instructions to the Students: 1. All questions are compulsory 2. Assume suitable data whenever necessary			
Q.1	Attempt all questions .	(/CO)	Marks
	1. BIOS stands for _____ and DOS stands for _____	CO1	6
	2. for character input with echo , INT 21 require _____ parameter.	CO1	
	3. _____, _____, _____ are segment registers.	CO1	
	4. 8086 works in _____ mode and _____ mode operation .	CO1	
	5. the default segment registers assigned to the following registers ; (i) SP _____ ii)DI _____ (iii) IP _____ (iv)BP _____	CO2	
	6. ASSUME and DB,DW are _____ directives.	CO2	
Q.2	Solve Any Two of the following.		3 X 2
(A)	Draw block diagram of 8086.list major components.	CO1	
(B)	How 8086 generates 20-bit physical address?	CO3	
(C)	Explain arithmetic instructions of 8086	CO2	
Q.3	Solve Any One of the following.		8
(A)	Elaborate interrupt structure of 8086	CO2	
(B)	Determine the content of register AX, BX & CX after the each instruction is executed in the following program MOV CL, 04 MOV AX, 007EH MOV BX, 0404H ROL AX, CL AND AH, BH OR BL, AL	CO3	
*** End ***			

The grid and the borders of the table may be hidden before final printing.