Code: 9F00203

MCA II Semester Supplementary Examinations May/June 2019
COMPUTER ORGANIZATION
(For students admitted in 2009, 2010, 2011 & 2012 (LC), 2013, 2014, 2015 & 2016 only)

Time: 3 hours   Max. Marks: 60

Answer any FIVE questions
All questions carry equal marks
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1 (a) Write a brief note on floating point representation.
   (b) Explain about half adders.

2 (a) Explain in detail about memory hierarchy.
   (b) Give a note on direct mapping of cache memory.

3   Explain about micro program sequencer with a neat diagram.

4   With examples, explain about instruction formats with suitable examples.

5 (a) Explain about conditional and unconditional transfer.
   (b) Briefly explain about priority interrupt.

6 (a) Discuss the differences between I/O and memory bus.
   (b) Explain DMA controller with a neat sketch.

7   Explain about vector processing.

8 (a) Discuss about IPC.
   (b) What are shared memory multi processors? Explain in brief.

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