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(3)	11600	NEC-503	(2)	000	41
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		na. na antana antan		Firstr	Firs
Draw and explain the functional block diagran	9.	ysical address for hexamples.	segments. How is the 20 bit physical address for memory generated? Explain with examples.	anker'	stRa
Explain the command words of 8259.	<u>,</u> ∞	ling memory into	Explain the advantages of dividing memory into	s cho	nk
Write a program to generate a square wave our using 8086. Assume 5 MHz Clock frequency.	7.	n Maximum and 8086.	What is the difference between Maximum and Minimum mode of operation in 8086.	ice wv	cer.c
8233.		OUT instruction.	Draw the timming diagram for OUT instruction.	vw.F ਰ	on
WAP using 8086 to interface seven segment dis	6.		cycle and T-states.	irstRa	n
(v) DT		cycle, instruction	What do you mean by machine cycle, instruction	anker	
(iv) ORG			Attempt any five questions from this section	com	
(iii) EVEN		o volume set sie som	Section-B		
(ii) PUBLIC	•	syschronous and ion.	Draw frame structures for sy- asynchronous modes of transmission.	(j) <b>×</b> Dr: asy	
(i) EXTRN		gnals of 8086?	Explain the role of DT/R and DEN signals of 8086?	(i) Fii w.Fiz	
(b) Explain the following assembler directive			What is USART?	(h) rstRa Wh	
(a) Write a 8086 assembly language processing convert binary to BCD.	5.	size available is	64K byte memory if memory chip size available is 2048×1?	nker.cor	
Explain with an example the various types of a modes supported by 8086 microprocessors.	4.	s needed to design	What are the 8086 intererupt types?  Calculate the number of memory chips needed to design		

program to addressing

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display with

e of 500  $\mu$  s cy.

am of 8257.

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(c) Compare RS232C and RS422A standards.

Atampt any two questions from this section.

10a (a) Explain the programming model of 8085.

st (b) What are interrupts? What happens when an interrupt is encountered? Classify the interrupt of 8085.  $\frac{9}{10}$  (15x2=30)

various methods of genrating delay using suitable on instructions.

land (a) Explain the difference between RAM and ROM.

st (b) Differentiation 11. Explain the concept of timer and delay in 8086. Explain various methods of genrating delay using suitable

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