

**Code: 9D06102**

M.Tech I Semester Regular &amp; Supplementary Examinations February 2016

**EMBEDDED SYSTEM CONCEPTS**

(Common to DSCE, DECS, ECE, ES, VLSIES, ESVLSI and VLSIESD)

(For students admitted in 2011, 2012, 2013, 2014 &amp; 2015 only)

Time: 3 hours

Max Marks: 60

Answer any FIVE questions  
All questions carry equal marks

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- 1 Explain the following concepts:
  - (a) Classification of embedded systems with examples.
  - (b) System on chip.
- 2 Explain the structure of CPU bus and explain the following bus operations with the help of timing diagrams:
  - (a) Read and Write.
  - (b) Burst transfer.
- 3 Explain about the following with an example:
  - (a) Round robin.
  - (b) Round robin with interrupts.
- 4 Explain in detail about linkers and locations for embedded software.
- 5 Draw and explain architecture of Kernel.
- 6
  - (a) Write the differences between RISC and CISC.
  - (b) Explain the instruction of ARM processor.
- 7 Explain about the following:
  - (a) Requirement analysis.
  - (b) System analysis.
  - (c) Architecture design.
- 8 Explain the designing of:
  - (a) INK – JET printer.
  - (b) Personal digital assistants.

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