

Code: 9D06102

M.Tech I Semester Supplementary Examinations February/March 2018

EMBEDDED SYSTEM CONCEPTS

(Common to DSCE, DECS, ECE, ES, VLSIES, ESVLSI & VLSIESD)

(For students admitted in 2012, 2013, 2014, 2015 & 2016 only)

Time: 3 hours

Max. Marks: 60

Answer any FIVE questions
All questions carry equal marks

- 1 Explain the design process flow in embedded system.
- 2 Describe the different types of memory devices used in embedded computing platform.
- 3 Elaborate on memory management system to save memory space in embedded computing environment.
- 4 Discuss the process flow, how embedded software is developed and getting into the target system.
- 5 Explain how pipes are applied to carry out the inter process communication in RTOS.
- 6 Describe the instruction set of ARM processor with functional features.
- 7 Discuss the architectural design concepts used for embedded system design.
- 8 Apply the embedded design concepts and explain the functions of Ink Jet printer.
