



Code: 9D06201

M.Tech II Semester Supplementary Examinations February 2018

MICRO COMPUTER SYSTEM DESIGN

(Common to DSCE, DECS & ECE)

(For students admitted in 2012, 2013, 2014, 2015 & 2016 only)

Time: 3 hours

Max. Marks: 60

Answer any FIVE questions
All questions carry equal marks

- 1 (a) Draw a neat architecture diagram of 8086 and explain its components.
(b) Discuss about instruction set of 8086 processor.
- 2 (a) Describe the addressing modes of 80286.
(b) Discuss in detail about registers in 80286.
- 3 (a) Explain the functions of memory management unit in 80486.
(b) Describe about memory paging mechanism followed in 80486.
- 4 (a) Describe about Branch prediction logic in Pentium processor.
(b) Explain about I/O system organization in Pentium processor.
- 5 (a) Differentiate Dual core and Single core microprocessor.
(b) With a neat diagram explain architecture of Pentium IV and its components.
- 6 (a) Explain about direct memory access method with a neat diagram.
(b) Describe about how to design an I/O system. Give an example.
- 7 (a) Explain how the processes can be managed in a computer.
(b) Describe about virtual memory concept in 80286.
- 8 (a) Explain about internal structure of 8087 in detail.
(b) Describe about instruction set of 8087 arithmetic coprocessor.

