Code: 9D06202

M.Tech II Semester Supplementary Examinations February 2018
HI-SPEED NETWORKS
(Common to DSCE & DECS)
(For students admitted in 2012, 2013, 2014, 2015 & 2016 only)

Time: 3 hours   Max. Marks: 60

Answer any FIVE questions
All questions carry equal marks

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1   Explain in detail about layered architecture of the high speed networks.

2   (a)   Discuss about the ISDN architecture in with suitable functional blocks and diagrams.
    (b)   Compare and contrast B-ISDN with D-ISDN.

3   Discuss about the virtual channels and its header format in ATM networks.

4   (a)   Explain in detail about ATM header and transmission protocols.
    (b)   Discuss about the congestion control in ATM frame relay protocols.

5   Explain about three types of routing algorithms in detail.

6   Discuss bout the salient features and architectural advantages of rearrangeable class networks.

7   Explain about two types of ATM addressing modes with the examples.

8   (a)   Discuss about the evaluation of QOS in IP.
    (b)   Explain about the TCP/IP protocols and its batch processing format.

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