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Register Number:

Name of the Candidate:

M.C.A. DEGREE EXAMINATION, May 2015

(SECOND SEMESTER)

241. COMPUTER ARCHITECTURE AND MICROPROCESSORS

Time: Three hours Maximum: 100 marks

SECTION -A **(8 × 5 = 40)**

Answer any EIGHT questions

1. Write a note on ASCII codes.
2. Difference between Assembly & High level languages.
3. Draw a pin diagram for MPU memory.
4. What is a decoder and demultiplexer?
5. What are the various data transfer instructions used in 8085? Explain.
6. What is data masking? Explain using examples.
7. What is a counter? Explain a counter with time delay.
8. Write a note how to convert BCD to 7 segment display.
9. Write short note on interfacing memory I/O and Mapped I/O.
10. Explain data acquisition system I/O controls.

SECTION -B **(3 × 20 = 60)**

Answer any THREE questions

11. Write short notes on (i) TTL Gates (ii) Decoder (iii) Registers & Counters (iv) Multiplexer
12. a) Explain TTL gates
    b) Write any five Boolean algebra laws & explain them with truth table implementation.
13. What is instruction format? Explain about the different types of instruction formats used in 8085 with examples.
14. Write short notes on (i) Stack operation (ii) Subroutine.
15. a) Explain temperature monitoring system.
    b) Explain the closed loop processing control.
