**Total No. of Pages:** 1

**7258**

Register Number:

Name of the Candidate:

# M.C.A. DEGREE EXAMINATION, May 2015

## (FIRST SEMESTER)

### 131. DIGITAL LOGIC AND COMPUTER ORGANIZATION

Time: Three hours                                                Maximum: 100 marks

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**SECTION -A**                                                    **(8 × 5 = 40)**
**Answer any EIGHT questions**

1. What are the registers? How they are used?
2. Define flip flop with a diagram.
3. What is RAM? Explain.
4. Define Addressing Mode.
5. What are instruction formats?
6. Define memory cycle time.
7. What is micro programmed control?
8. List the factors determine the control signals.
9. What is memory mapped I/O?
10. What is DMA controller?

**SECTION -B**                                                    **(3 × 20 = 60)**
**Answer any THREE questions**

11. Discuss the following: (i) Multiplexer (ii) De-Multiplexer.
12. Explain in detail about virtual memory management techniques.
13. Discuss in detail about various instruction formats.
14. Explain the concept of hardwired control unit.
15. Explain in detail about DMA controller.

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