Code: R7310504

R07

III B. Tech I Semester (R07) Supplementary Examinations, May 2012

MICROPROCESSORS & INTERFACING

(Common to CSE, IT & ECC)

Time: 3 hours Max Marks: 80

Answer any FIVE questions
All questions carry equal marks
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1 (a) With examples explain about the addressing modes of 8086 microprocessor.
(b) Explain how DMA is implemented in 8085.

2 (a) Write an ALP in 8086 to find the largest of a set of 8 bit numbers.
(b) Write an ALP in 8086 to subtract two 8 bit hexadecimal numbers.

3 (a) With relevant pin diagrams, explain about the minimum and maximum mode operations of 8086 microprocessor.
(b) Explain briefly how DMA is implemented.

4 (a) Explain the need for actuators, A/D and D/A converters in microprocessor based systems.
(b) Give the BSR control word format of 8255 and explain about each bit.

5 (a) Discuss briefly about the interrupt system of 8086. What is interrupt pointer? What is 'type' of an interrupt?
(b) Why is it necessary to send an End-of-Interrupt (EOI) command to an 8259 A at some time in an interrupt service routine?

6 (a) Briefly explain about TTL to RS 232C and RS 232 C to TTL conversion.
(b) Discuss briefly about the methods of data communication.

7 (a) Draw and discuss the structure of an 80386 descriptor.
(b) Briefly explain the enhanced instruction set of Pentium.

8 (a) Explain the addressing modes supported by 8051.
(b) Discuss briefly about 8051 oscillator and clock.

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