Code: R7411310

R07

B.Tech IV Year I Semester (R07) Supplementary Examinations December 2015
EMBEDDED & REAL TIME SYSTEMS
(Electronics & Control Engineering)
(For 2008 regular admitted batch only)

Time: 3 hours   Max. Marks: 80

Answer any FIVE questions
All questions carry equal marks
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1   Explain in detail about IC technology.

2   Write short notes on the following:
   (a) Pipelining.
   (b) Interrupts.
   (c) Debuggers.
   (d) Emulators.

3 (a) Compare models Vs languages.
   (b) Describe in detail about concurrent process model.

4 (a) Discuss in brief about RS-232 communication parameters.
   (b) With a neat sketch explain the different layers in Ethernet LAN protocol architecture.

5 (a) Explain the following scheduling algorithms:
         (i) First-in first out.
         (ii) Round-robin with priority.
   (b) Write a brief note on semaphores.

6 (a) Briefly explain about event registers.
   (b) List the function calls to manage a signal.
   (c) List the applications of message queues.

7 (a) Explain in brief about embedded Linux.
   (b) Write a brief note on palm OS.

8 (a) Draw and explain Gajski Y-chart.
   (b) Describe in detail about Hardware-Software Co-Simulation.

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