

Code: R7310201

B.Tech III Year I Semester (R07) Supplementary Examinations December 2015

COMPUTER SYSTEM ORGANIZATION

(Electrical & Electronics Engineering)

(For 2008 regular admitted batch only)

Time: 3 hours

Max. Marks: 80

Answer any FIVE questions
All questions carry equal marks

- 1 (a) What are the different types of computers? Explain.
(b) Explain in detail about the floating point representations.
- 2 With a block diagram construct a common bus system for four registers with multiplexers. Explain its function table.
- 3 (a) Explain LAD and STA memory-reference instructions with suitable examples.
(b) Explain two-address and three address instructions with suitable examples.
- 4 (a) Describe in detail about the micro programmed control organization.
(b) Discuss in detail about the five fields of symbolic micro instruction.
- 5 (a) Explain briefly about the semiconductor ROM memories.
(b) Write a short note on virtual memories.
- 6 (a) What are the major differences that exist between the central computer and peripherals?
(b) Explain in detail about the source initiated transfer using handshaking.
- 7 Explain the general structure of four segment pipeline and illustrate the behavior of pipeline with a space-time diagram.
- 8 (a) What are the characteristics of multiprocessors? Explain.
(b) Write a short note on multistage switching network.
