Code: R7310402

R07

B.Tech III Year I Semester (R07) Supplementary Examinations December 2015

COMPUTER ORGANIZATION

(Common to ECE, EIE and E.Con.E)

(For 2008 regular admitted batch only)

Time: 3 hours

Max Marks: 80

Answer any FIVE questions

All questions carry equal marks

*****

1 (a) What is meant by system software?
  (b) Explain various functions of systems software.

2 (a) Explain about the graphic symbols of three state buffer gate.
  (b) Describe the mechanism used in register transfer.

3 (a) What is data path? Explain.
  (b) Draw and explain the implementation of one-bit register.

4   Draw and explain Flow-chart for Division operation of floating point numbers.

5 (a) Describe the various types of semiconductor memories.
  (b) What is virtual memory? What is the relation between address and memory space in a virtual memory system? Explain with the help of example.

6 (a) Differentiate between Isolated I/O and memory mapped I/O.
  (b) Discuss in detail about asynchronous serial transfer.

7 (a) What is pipelining? Demonstrate the pipeline organization with an example.
  (b) Explain how matrix multiplication is performed with vector processors.

8 (a) Explain the characteristics of Multiprocessors.
  (b) Explain how synchronization is achieved in multiprocessor systems.

*****