

R09**Code: 9A05406****B.Tech III Year I Semester (R09) Supplementary Examinations June 2016****COMPUTER ORGANIZATION**

(Electronics and Communication Engineering)

Time: 3 hours

Max Marks: 70

Answer any FIVE questions

All questions carry equal marks

- 1 (a) Discuss in brief about Multiprocessors and Multicomputers.
(b) Explain the pipelining and superscalar operation.
- 2 (a) With a neat sketch, explain the operation of n-bit parallel adder.
(b) Explain the difference between logic shift and arithmetic shift.
- 3 (a) With the help of a diagram, clearly explain the functioning of micro programmed control unit.
(b) What is subroutine? What is the function of sub routine register?
- 4 (a) Explain the floating point multiplication algorithm with flow chart.
(b) Consider the floating point format with 8 bits for the biased exponent and 23 bits for the significant. Represent the following numbers in the above format: (i) -720. (ii) 0.645.
- 5 Give short notes on the following:
(a) Bootstrap Loader.
(b) Auxiliary memory.
(c) Data stripping
- 6 (a) Explain about data transfer modes?
(b) List the salient features about standard serial communication protocols
- 7 (a) Differentiate RISC and CISC computers.
(b) Explain RISC pipelining
- 8 Explain Interprocessor communication and Synchronization
