No: 126EM

R13

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech III Year II Semester Examinations, May - 2016 MICROPROCESSORS AND MICROCONTROLLERS

me: 3 hours	6			M	Max. Marks: 75	
Part A consist	is compulsors of 5 Units. A	ontains two parts which carries 25 caswer any one fulve a, b, c as sub q	marks. Answer	r all questions in	Part A. Part B	* X X X X X X X X X X X X X X X X X X X
A X X X X X X X X X X X X X X X X X X X	* * * * * * * * * * * * * * * * * * *	PAI	RT - A		(25 Marks)	A H K B B A A B K
b) Draw to c) Explair d) List the e) Draw to f) Explair g) Explair h) List out i) Draw to	he flag register one byte and data transfer in BSR mode on the concept of register set of the difference to TO and T1 in	mentations present r of 8086 Micropr two byte instructionstruction set of 8 frame format. of interrupt service f 8051 Microcontres between micropresent services interrupts of 8051 not interrupts of 805	ocessor and exploon frame format 8086 microproce routine of 8086 coller. cocessor and microcontroller.	ssor. microprocessor. rocontroller.	[2] [3] [3] [2] [3] [2]	Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y Y
		PAR	(1 - B		(50 Marks)	
b) Explain c) Explain a) Draw th	the minimum the concept one internal arcl	anization of 8086 mode pins of 808 f physical address (nitecture of 8086 r ram of minimum r	66 Microprocesso calculation of 8 DR microprocessor a	or in detail: 086 microprocess and explain its ope	eration.	M N N N N N N N N N N N N N N N N N N N
Microp	rocessor with	node and explai examples sembler directives	* * **	* x *x	* * **	M H H H H H H H H H
a) Write a number	n assembly la s. King	Inguage program	to find the large in the set of 8086	est number in an	array of 8-bit	M M • M • M • M • M • M • M • M • M • M

11/11/11	Draw the internal architecture of 8255 PPI and explain its operation. Draw the interacting diagram of A/D convertor with 8086 microprocessor and explain its operation. [5+5]											
a) Explain the concept of keyboard and interfacing along with block diagram. b) Explain the concept of methods of serial communication with examples. [5+5] 8.a) Draw the internal architecture of 8051 Microcontroller and explain its operation. b) Draw the PSW and TCON registers of 8051 microcontroller. [5+5] OR												
b)	Draw the	e different future pin diagram of 8	8051 microcontro		the function of	each pin in	X X X X X X X X X X X X X X X X X X X					
		e different addre SCON register fr			controller with ex	amples. [5+5]						
11.a) b)	List out th Write an a	e different instru ssemble languag	ction set of 8051 e program for LI	microcontroller ED blinking in 8	and explain with 051 microcontrol	n examples. ller[5+5]	X					
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X	**	K9	K9	K9	K9	K9	X					
+ + + × × × × × × × × × × × × × × × × ×	**	K9	* * *** * * * * * * * * * * * * * * * * *		k W	K9	* 6 * X X X X X X X X X X X X X X X X X					