I B.Tech Examinations,December 2010

## ELECTRICAL CIRCUIT ANALYSIS

Common to Instrumentation And Control Engineering, Electrical And Electronics Engineering
Time: 3 hours
Max Marks: 80
Answer any FIVE Questions
All Questions carry equal marks

1. Find the Z and Y parameters of the circuit shown in figure 2.


Figure 2
2. Draw the graph of the lattice network shown in the figure 7. Deduce the tie-set and cut-set matrices, and from those, find the relationship between independent and dependent variables.



Figure 7
3. Two impedances $(15-\mathrm{j} 10) \Omega$ and $(10+\mathrm{j} 15) \Omega$ are connected in parallel. The supply voltage is $200 \mathrm{~V}, 50 \mathrm{~Hz}$. Calculate
(a) The admittance
(b) Conductance
(c) Susceptance of the combined circuit
(d) Total current and
(e) Total power factor
(f) Total power consumed is each branch and total power.
4. Derive an expression for current response of RLC series circuit with sinusoidal excitation. Assume the circuit is working in critical damping conditions.
5. (a) Calculate load current $I_{L}$ using Millman's theorem as shown in figure 4a.


Figure 4a
(b) State and explain the theorems in voltage and current source representations.

$$
[6+10]
$$

6. (a) Derive an expression for the energy stored in an inductor and a capacitor.
(b) Obtain an expression for Co-efficient of coupling.
[10+6]
7. A three phase, 4 wire, $400 \mathrm{~V}, 50 \mathrm{~Hz}$ system supplies a load of 50 KW at a power factor of 0.75 lagging between the red phase and neutral; 60 KW at a power factor of 0.85 leading between the yellow phase and neutral and 70KW at unity power factor between the blue phase and neutral. Find the current in the neutral conductor.
8. (a) State and explain the Kirchhoff's Law which can be applied to loop current method.
(b) For the network shown in figure 1b, determine the equivalent resistance between the terminals A and B , if each resistance value is equal to 1 ohms.


Figure 1b


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(b) For the network shown in figure 1b, determine the equivalent resistance between the terminals A and B , if each resistance value is equal to 1 ohms.


Figure 1b
3. (a) Calculate load current $I_{L}$ using Millman's theorem as shown in figure 4a.


Figure 4a
(b) State and explain the theorems in voltage and current source representations.
4. Find the Z and Y parameters of the circuit shown in figure 2.


Figure 2
5. Draw the graph of the lattice network shown in the figure 7. Deduce the tie-set and cut-set matrices, and from those, find the relationship between independent and dependent variables.


Figure 7
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1. Draw the graph of the lattice network shown in the figure 7. Deduce the tie-set and cut-set matrices, and from those, find the relationship between independent and dependent variables.


Figure 7
2. (a) Derive an expression for the energy stored in an inductor and a capacitor.
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4. Find the Z and Y parameters of the circuit shown in figure 2.


Figure 2
5. (a) State and explain the Kirchhoff's Law which can be applied to loop current method.
(b) For the network shown in figure 1b, determine the equivalent resistance between the terminals A and B , if each resistance value is equal to 1 ohms.
$[6+10]$


Figure ib
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Figure 1b
2. Find the Z and Y parameters of the circuit shown in figure 2.


Figure 2
3. (a) Derive an expression for the energy stored in an inductor and a capacitor.
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[16]
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