# III B.Tech II Semester Examinations,December 2010 MICROPROCESSORS <br> Mechatronics 

Max Marks: 80
Time: 3 hours

## Answer any FIVE Questions <br> All Questions carry equal marks

1. (a) How many initialization command words are required for a single 8259 in an 8086 based system? Explain their format?
(b) Under what conditions type 0 interrupt is initiated? List out the instructions that may cause type 0 interrupt?
[8+8]
2. (a) Explain the Flag register of 8085 Microprocessor.
(b) Explain the following pins of 8085 Microprocessor.
i. RST 5.5
ii. RST 6.5
iii. READY
iv. $\overline{I N T A}$
v. $X_{1}, X_{2}$

$$
[6+10]
$$

3. (a) Define a macro for mowing an arbitrary character string that ends with an EOT character from one string of bytes in memory to another?
(b) Write $\hat{a}$ procedure COMPUTE for performing the computation $\mathrm{R} \leftarrow \mathrm{X}+$ $\mathrm{Y}-3$ The word variables $\mathrm{X}, \mathrm{Y}, \mathrm{R}$ and COMPUTE are in the same code segment. The variables $X$ and $Y$ are defined in data segment D1_ SEG. The data segment D2_ SEG contains the variable R. Show the necessary definition along with the procedure?
[8+8]
4. (a) Draw the command register and mode register format of 8237 and explain each bit?
(b) 8251 is interfaced to 8086 processor at address 080 H . Show the hardware design? Initialize it in asynchronous mode with even parity, 6 -data bits, baud rate factor 1 , one start bit and one and half stop bits? [8+8]
5. Interface an 8 -bit DAC to 8255 with an address map of 0800 H to 0803 H . The DAC provides output in the range of +12 V to -12 V . Write the instruction sequence for the following.
(a) For generating a square wave with a peak to peak voltage of 8 V and the frequency will be selected from memory location 'FREQ'.
(b) For generating a triangular wave with a maximum voltage of +6 V and a minimum of -4 V .
6. A logic network is having input variables $\mathrm{A}, \mathrm{B}, \mathrm{C}, \mathrm{D}$. The output variables are given below.

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\begin{aligned}
& \mathrm{W}=\overline{\mathrm{A}} \cdot \overline{B C}+B C D+A \bar{D} \\
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The array INPUT_1 contains 10 different combinations of input variables. Write an instruction sequence that determine the outputs for each combination of INPUT_1 array and store the output variables in the string OUTPUT_1.
7. (a) Draw and explain the pin out diagram of 8086 .
(b) Explain the various operations performed by Bus Interfacing unit in 8086.

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[10+6]
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8. (a) Write a program to find the Parity of 48 bit Number.
(b) Write a program to solve the following expression. $2 A^{2}+4 A B+B^{2} C \quad[8+8]$

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