

Code No: RT32053

R13**SET - 1****III B. Tech II Semester Supplementary Examinations, November - 2017****COMPUTER NETWORKS**

(Common to Computer Science Engineering and Information Technology)

Time: 3 hours

Max. Marks: 70

- Note: 1. Question Paper consists of two parts (**Part-A** and **Part-B**)
2. Answering the question in **Part-A** is compulsory
3. Answer any **THREE** Questions from **Part-B**

PART -A

- 1 a) Discuss the TCP Layers. [3M]
- b) Explain Virtual Circuit Networks. [4M]
- c) Discuss the CDMA. [4M]
- d) Distinguish between One bit and Stop – wait protocol. [4M]
- e) Discuss the MAC sub layer Design issues? [3M]
- f) Write the HTTP Response message formats. [4M]

PART -B

- 2 a) Discuss the Novell Networks. [4M]
- b) Explain in detail ISO/OSI network model with near sketch. [8M]
- c) Write short notes on MAN Network topologies. [4M]
- 3 a) What is meant by multiplexing? Describe different types of multiplexing. [3M]
- b) Briefly explain the statistical time division multiplexing? [8M]
- c) Explain Circuit Switched Networks with suitable example. [5M]
- 4 a) Explain Sliding window protocol with suitable example. [8M]
- b) Describe and discuss the data link layer design issues. [8M]
- 5 a) Explain Token Passing Controlled Access method. [8M]
- b) Explain shortest path routing with suitable example. [8M]
- 6 a) What is a Fast Ethernet? Explain in detail Fast Ethernet. [8M]
- b) Briefly discuss the MAC addressing mechanism. [8M]
- 7 a) Discuss and draw the Wireless Application Protocol stack. [8M]
- b) Explain HTTP Operational Model communication in WWW. [8M]
