Code No: R22054   **R10**   SET - 1

II B. Tech II Semester Supplementary Examinations, April/May - 2017
COMPUTER ORGANIZATION
(Com. to CSE, ECC)

Time: 3 hours   Max. Marks: 75

Answer any **FIVE** Questions
All Questions carry **Equal** Marks
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1. Explain 8085 Microprocessor Instruction set Architecture

2. a) What are the functions of ALU and explain.

   b) Explain the components of the computer system.

3. a) What is Interrupt? Explain Input output Interrupts.

   b) What is instruction cycle? Briefly explain with the help of state diagram.

4. a) Explain the various addressing modes with examples.

   b) Explain micro instruction sequencing in detail.

5. a) Discuss decimal arithmetic operations.

   b) Explain Booth multiplication algorithm with example.

6. a) Explain internal organization of memory chips.

   b) Explain the address translation in Virtual Memory.

7. a) Explain the characteristics of multiprocessors.

   b) Discuss about Dynamic arbitration algorithms.

8. a) Explain Parallelism in Uniprocessor Systems?

   b) What is the use of priority interrupt?