



SET - 1

II B. Tech II Semester Regular/Supplementary Examinations, April/May-2017 ELECTRONICS CIRCUIT ANALYSIS

(Com. to ECE, EIE)

Time: 3 hours

Max. Marks: 70

Note: 1. Question Paper consists of two parts (**Part-A** and **Part-B**) 2. Answer **ALL** the question in **Part-A**

3. Answer any THREE Questions from Part-B

-----<u>PART –A</u>

1.	a)	Define the h-parameter of the Transistor. Draw a h-parameter network	
		representation of a transistor	(4M)
	b)	What is Darlington Transistor. What are its sailent features	(4M)
	c)	What is meant by feedback in amplifiers, what are the types of feedback	(4M)
	d)	What are the constituent parts of an Oscillator	(4M)
	e)	How are amplifiers classified based on the biasing condition	(3M)
	f)	Mention the application of Class-C tuned amplifier	(3M)

<u>PART –B</u>

2.	a)	With a neat sketch explain about FET	(8M)
	b)	A FET has Drain saturation current I_{DSS} of 10mA and Quiescent point Drain surrent L is 5mA with ringh off values $Y = 4Y$ salevlate the value of Y and	(8M)
		current I _D is SinA, with pinch –off voltage $v_p=4v$, calculate the value of v_{GS} and the value of Transconductance q	
		the value of Transconductance g _m .	
3.	a)	Explain about Boot-strap follower	(8M)
	b)	Differential amplifier using BJT	(8M)
4.	a)	With a neat sketch explain a negative feedback amplifier and obtain expression for	
		its closed loop gain	(8M)
	b)	An amplifier requires an input signal of 60mV to produce a certain output. with a	(8M)
		negative feedback to get the same output the required signal is 0.5V. The voltage	
		gain with feedback is 90. Find the open loop gain and feedback factor	
_			
5.	a)	Draw the circuit of Hartley oscillator and explain its working. Derive the	
	1 \	expressions for frequency of oscillation and condition for starting of oscillation	(8M)
	b)	In an Hartley oscillator , if $L_1=0.2$ mH, $L_2=0.3$ mH and C=0.003 µF, calculate the frequency of its oscillation	(8M)
6	a)	What is Heat sink, explain the different types of Heat sinks	(9M)
0.	a) b)	Determine the power dissipation capability of a transistor, which has been	(0NI)
	0)	mounted with a heat sink having thermal resistance $\Theta_{\text{HS-A}}=8^{\circ}\text{c/w}$, $T_{\text{A}}=40^{\circ}\text{c}$, $T_{\text{J}}=160^{\circ}\text{c}$, $\Theta_{\text{J-C}}=5^{\circ}\text{c/w}$ and $\Theta=85^{\circ}\text{c/w}$	(011)
7.	a)	Define O-factor, derive the expressions for O factors of RL and RC circuits	(8M)
	b)	Explain about the stability of Tuned amplifiers	(8M)

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SET - 2

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Time: 3 hours

Max. Marks: 70

Note: 1. Question Paper consists of two parts (Part-A and Part-B) 2. Answer ALL the question in Part-A

3. Answer any THREE Questions from Part-B

PART –A

1.	a)	Write the elements of the Hybrid- π model	(4M)
	b)	With a neat sketch explain about Multi stage amplifier	(4M)
	c)	How does negative feedback reduce distortion in an amplifier	(4M)
	d)	What are factors which affects the frequency stability of an oscillator	(4M)
	e)	Why is non-linear distortion called Harmonic distortion	(3M)
	f)	Define a Q-factor of a resonant circuit	(3M)

Define a Q-factor of a resonant circuit f)

PART -B

- 2. Define Hybrid- π model. Draw and derive the expressions for different elements of (16M) the Hybrid $-\pi$ model (i) Determination of Trans Conductance (ii) Determination of input conductance (iii) Determination of feedback conductance (iv) Determination of output conductance
- 3. Explain about the different Coupling schemes used in amplifiers with diagrams (16M)
- What are the different types of negative feedback, explain how the input and (8M) 4 a) output impedances of an amplifier are affected by the different types of negative feedback
 - b) The distortion in an amplifier is found to be 3%, when the feedback ratio of a (8M) negative feedback amplifier is 0.04, when the feedback is removed, the distortion becomes 15%. Find the open loop gain and closed loop gain
- Write down the expression for frequency of oscillation in Hartley and Colpitts 5. a) (8M) Oscillators
 - A Colpitts Oscillator is designed with C₂=100pF and C₁=7500pF.The inductance (8M) b) is variable, determine the range of inductance values, if the frequency of oscillation is to vary between 950 and 2050 KHz
- What is meant by distortion in power amplifiers, explain the given different types 6. a) (8M) of distortions b) With a neat sketch explain about push-pull amplifier (8M)
- a) Draw the equivalent circuit of capacitance coupled single tuned amplifier and 7. (8M) derive the equation for voltage gain
 - b) Differentiate the single tuned and double tuned amplifiers (8M)

1 of 1

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SET - 3

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amplifiers (ii)Double tuned amplifiers





SET - 4

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3. Answer any **THREE** Questions from **Part-B**

-----<u>PART –A</u>

1.	 a) b) c) d) e) f) 	Explain about validity of Hybrid $-\pi$ model What are DC amplifiers, mention a few applications of DC amplifiers An amplifier with stage gain 200 is provided with negative feedback of feedback ratio 0.005, find the new gain Explain the difference between an amplifier and an Oscillator What is the difference between crossover distortion and inter modulation distortion	(4M) (4M) (4M) (4M) (3M)
	1)	<u>PART –B</u>	(311)
2.	a)	Draw and Explain about the small signal high frequency equivalent circuit of a Transistor	(8M)
	b)	If a Transistor has a value of β =50 and Collector current of 10mA,determine the value of Emitter Current and Calculate the value of α of the Transistor	(8M)
3.	a) b)	With a neat sketch explain about the cascade amplifiers A CE-RC coupled amplifier uses transistor with the following h-parameters: $h_{fe}=50$, $h_{oe}=30 \times 10^{-6}$ mhos, $h_{re}=2.5 \times 10^{-4}$. The value of g_m at the operating point is 50m mhos. The biasing resistor R1 between V_{cc} and base is 100K Ω and R_2 between base and ground is 10K Ω . The load resistor $R_C = 5K\Omega$. let $C = 160 pF$ be the total shunt capacitance in the input circuit and the coupling capacitor $C_c=6\mu F$, Calculate for one stage of the amplifier (i) mid-band current gain (ii) mid-band voltage gain	(8M) (8M)
4.	a) b)	Explain the method of identifying feedback Topology A voltage-series negative feedback amplifier has a voltage gain without feedback of A-500,input resistance $R_i=3K\Omega$,output resistance of $R_o=20K\Omega$ and feedback ratio $\beta=0.01$,calculate the voltage gain A_f , input resistance R_{if} and output resistance R_{of} of the amplifier with feedback	(8M) (8M)
5.		Explain the working of a (i) Miller Crystal Oscillator (ii) Pierce crystal oscillator	(16M)
6.	a) b)	What is Class A amplifier ,Derive the expression for maximum value of efficiency $V_{CE(max)}=15V$, $V_{CE(min)}=1V$, find the overall efficiency for (i)series –fed load(ii)transformer-coupled load	(8M) (8M)
7.	a) b)	What is a Q-factor, Derive the expression for Q-factor of a capacitor Explain the offect of according ingle tuned amplifiers on Bandwidth	(8M) (8M)