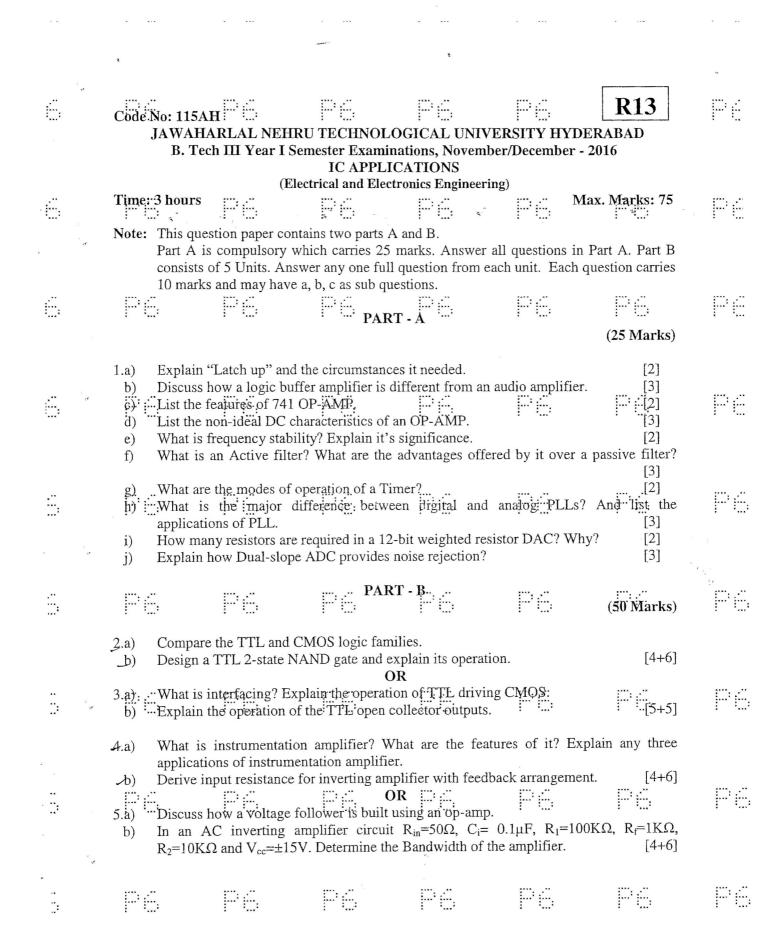
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						, i.e.
7.a)]	Derive the expression for the Draw the schematic diagrant Design a first order active the Why this is called Active to Design a Triangular wave	OR high pass filter value filter? generator with f_0	with cutoff freque	ency of $2KHz$ with $O(PP) = 5V$.	ith op-amp.	F'E
b)]	Describe the 555 timer mo Design a 555 timer circu Signal frequency is 4KHz.	onostable multivil bit whose output	prator application frequency is 2K	in pulse stretch Hz when the tr	ing inger input [5+5]	Pki
9-a)]	Explain the operation of for Define Lock-in range, Cap	requency multipli other range and P	er using PLL. ull-in time in PL	Lisystem.	[: :[6+4]	
10.a) (b) 1	Compare the dual slope A Explain the operation of R	DC with successions and successions and successions are successions.	with the help of	n ADC. neat diagrams.	[4+6]	
11.a) 1	Explain the operation of fl What are the merits and do	lash ADC using r emerits of counte	elevant diagrams r type ADC? Exp	ola i n	[:[6+4]	
ooOoo						
F'E	P6			P6	P6	F'E
						э.
F.E	P6	P6	P'6	P6	P6	PE
F'E	P6	P6	P6	P6	P6	PE
Pé	P6	P6	PS	P6	P6	PE
						я,
PE	. PG	P6	P6	PE	P6	F.E.