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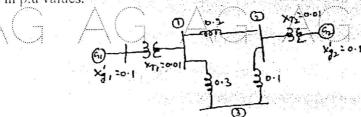
Code No: 126AG JAWAHARLAL NEHRU TECHN LOGICAL UNIVERSITY HYDERABAD B. Tech III Year 1 Semester Examinations, May - 2017 COMPUTER NETHODS IN POWER SYSTEMS (Electrical and Electronics Engineering) Time: 3 hours Max. Marks: 75 Note: This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions. PART - A Define the bus incidence matrix. [2] 1.a) What are the merits for formation Y_{bus} by direct inspection method? [3] b) What data is necessary for power flow studies? [2] c) What is the need of DC load flows? [3]
What are the various types of series reactors used for reducing the short circuit MVA? d) e) [2] What is the significance fault current calculation and which fault is more severe? [3] f) Define the stability limit in power system. [2] g) Define the transfer reactance and synchronizing power coefficient. [3] h) What are the various applications of equal area criterion? [2] i) What are the various methods to improve transient Stability? [3] j) (50 Marks) For the figure 1 shown below, the impedance data is given in table. Determine Y Bus 2. matrix by singular transformation method [10]This rection method? () () Figure 1 Table trons of equal area criterion? Bus code Self impedance Element in p.u 0-1 PART 0.1 2 0 - 20.2 3 0 - 30.3 4 0 - 40.35 5 1-2 nothed 0.4 0.1 6 3 - 40.2 0.15

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Find the Z Bus for the power system network shown in below figure 2. All reactance's are in ptu values. [10]



Explain the necessity of load flow solution.

b) Write an algorithm for Gauss seidal load flow method by considering all types of buses.

Figure 2

[3+7]

OR

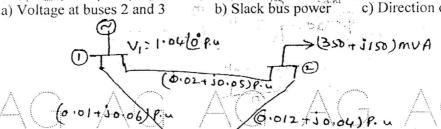
Single line diagram of a simple power system with generators at buses 1 and 3 shown in below figure 3. The necessary data are given in the figure. Line impedances are marked in p.u.on a 100MVA base. Determine the following using Fast-decoupled load flow method at the end of first iteration.

[4+3/43]

a) Voltage at buses 2 and 3

b) Slack bus power

c) Direction of line flows.



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Figure 3

A transformer rated at 30 MVA and having a short circuit reactance of 0.02 p.u is connected to the bus bar of a generating station which is supplied through two 12.6 kV feeders each having an impedance of (2±j/4) \(\Omega\) One of the feeder is connected to the generating station using generator capacity of 40 MVA connected to its bus bars having a short circuit reactance of 0.12 p.u and other feeder to a generator with 30MVA and having a reactance of 0.3 p.u. Find the kVA supplied to the fault in the event of a short circuit occurring between the secondary terminals of the transformer.

7.a) What do you understand by sequence network? What is their importance in unsymmetrical fault calculations?

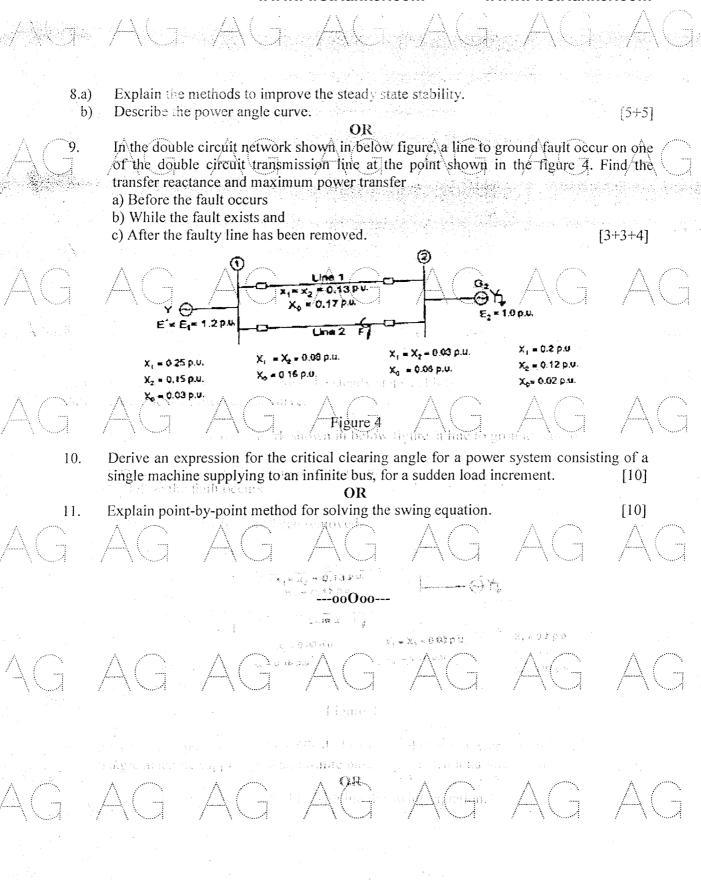
b) A generator rated 100MVA, 12.6 kV has $X_1 = X_2 = 25\%$ and $X_0 = 10\%$. Its neutral is grounded through a reactance of 0.2 Ω . The generator is operating at rated voltage, load is disconnected from the system when single line to ground fault occurs at its terminals. Find the sub-transient current in the fault phase and line to line fault current.

of 0.1.1 p.u and other feeder to a generator with \$1.4.



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