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## **R16** Code No: 134AM JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B.Tech II Year II Semester Examinations, April - 2018 **CONTROL SYSTEMS** (Common to EEE, ECE, EIE) Time: 3 Hours Max. Marks: 75 **Note:** This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions. PART- A 1.a) Write the Manson's gain formula. [2] What are the basic properties of SFG? b) [3] What are the standard test signals used in control systems? c) [2] d) Distinguish between type and order of a system. [3] Define a stable system. [2] e) f) Explain the basics of root locus plot. [3] What is polar plot? [2] g) Define gain and phase margins. h) [3] i) What is state diagram? [2] j) Mention any four advantages of state variable representation. [3] **PART-B** (50/Marks) Compare the AC and DC servomotors. For the system represented by the block diagram shown in figure 1. Find $\frac{C}{R}$ . b) [4+6] $H_2$ $H_1$ Figure: 1



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Find the overall gain  $\frac{C(s)}{R(s)}$  for the signal flow graph shown in figure 2. b)

[4+6]



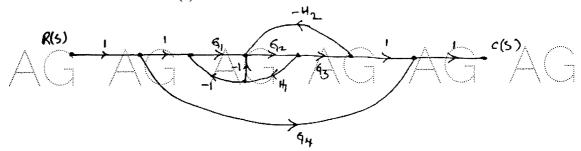


Figure: 2

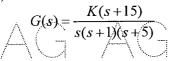
The open-loop transfer function of a unity feedback control system is given by Find the natural frequency of response, damping ratio, damped [10] frequency and time constant.

OR

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For unity feedback control system the open loop transfer function, G(s) =Find the  $e_{ss}$  when the input is  $r(t) = 3 - 2t + 3t^2$ . And find  $K_p$ ,  $K_v$ , and  $K_a$ .

- Determine the RH stability of given characteristic equation,  $s^4 + 8s^3 + 18s^2 + 16s + 5 = 0$ . 6.a)
- Sketch the root locus of the system, whose open loop transfer function is, b)







Find K so that the system is stable with, a) GM=2db, 7. b)  $PM=45^{0}$ 

[10]

9.

The open loop transfer function is given by G(s)H(s)stability of closed loop system.

The open-loop transfer function of a system is given by

Design a lag-lead compensator to meet the  $K_v=100 \text{sec}^{-1}$  and Phase margin  $\geq 30^{\circ}$ ./[10]



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