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SET - 1

(7M)

Code No: R1622041 (R16)

II B. Tech II Semester Supplementary Examinations, November - 2018 **ELECTRONIC CIRCUIT ANALYSIS** (Com to ECE, EIE) Time: 3 hours Max. Marks: 70 Note: 1. Question Paper consists of two parts (Part-A and Part-B) 2. Answer ALL the question in Part-A 3. Answer any **FOUR** Questions from **Part-B** PART -A 1. (3M)a) Explain why RC Phase shift oscillators are not used at high frequencies. b) Which configuration is the best in cascade for an output stage and for an (2M)intermediate stage? c) (2M)Explain characteristics of negative feedback amplifiers. (2M)d) Derive the expression for overall gain of a negative feedback circuit. (2M) e) Derive the expression for harmonic distortion. f) (3M)Differentiate staggered tuned amplifiers and wideband amplifiers. PART-B Find the voltage gain, input and output resistances of a emitter follower at 2. a) (14M)high frequencies. Differentiate between direct and capacitive coupling of multiple stages of 3. (7M)a) amplifiers. With the help of a neat circuit diagram, describe the working of a cascade amplifier. Draw the circuit diagram, equivalent circuit of a Darlington pair and derive (7M)b) expressions for overall voltage gain and input impedance. 4. a) An amplifier has a gain of 50 with negative feedback. For a specified output (7M)voltage, if the input required is 0.1V without feedback and 0.8V with feedback, Compute β and open loop gain. Through the block schematics, show four types of negative feedback in b) (7M)amplifiers. 5. (7M) a) Derive the condition for oscillations? Discuss. Draw the RC-phase shift oscillator and derive the condition for oscillations. b) (7M)6. (7M)a) Write short notes on Thermal stability and Heat sinks. VCE(max)=15V,VCE(min)=1V, find the overall efficiency for (i) series –fed b) (7M)load (ii) transformer-coupled load 7. a) Draw the diagram of a capacitance coupled tuned amplifier and derive an (7M)expression for its quality factor.

b)

Show that Bandwidth decreases with cascading of single tuned amplifiers.