

Code No: R22054

R10**SET - 1****II B. Tech II Semester Supplementary Examinations, November-2017****COMPUTER ORGANIZATION**

(Com. to CSE, ECC)

Time: 3 hours

Max. Marks: 75

Answer any **FIVE** Questions
All Questions carry **Equal** Marks
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1. Explain assembly language Instructions and instructions set Architecture Design?
2. a) Explain the different type's arithmetic micro operations.  
b) Explain about shift micro operations.
3. Explain Micro Operations and Register Transfer Language in detail.
4. Design and Explain the implementation very Simple CPU and Relatively simple CPU?
5. a) Explain the decimal arithmetic operations.  
b) Derive and explain an algorithm for adding and subtracting 2 floating point binary numbers.
6. Explain Memory Management in Pentium/windows personal Computers?
7. What is direct memory Access? Explain Serial communication standards?
8. Explain Multiprocessing Operating Systems and Software?