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II B. Tech II Semester Supplementary Examinations, April-2018 ELECTRONICS CIRCUIT ANALYSIS

(Com. to ECE, EIE)

Note: 1. Question Paper consists of two parts (**Part-A** and **Part-B**)

2. Answer ALL the question in Part-A

3. Answer any THREE Questions from Part-B

PART -A

1.	a)	Derive Trance conductance (g_m) , Base spread resistance $(r_{bb'})$?	(4M)
	b)	List the types of coupling schemes?	(3M)
	c)	What is the effect of -ve feedback on the input impedance of an amplifier	(4M)
	d)	State Barkhausen's criteria	(3M)
	e)	What is crossover distortion? Explain.	(4M)
	f)	What is the effect of cascading in tuned amplifier? PART -B	(4M)
2.	a)	Draw approximate equivalent hybrid Π model for the calculation of the short	(8M)
2.	u)	circuit CE current gain and derive the same.	
	b)	With hybrid π equivalent circuit, derive the expressions for hybrid conductances.	(8M)
3.	a)	Explain different methods used for coupling multistage amplifiers with their	(8M)
	b)	frequency response. Five identical stages are coupled, each amplifier having $f_2=100$ KHz.Determine the overall upper cutt0off frequency for the five stages.	(8M)
4			
4.	a)	Explain different negative feedback topologies with neat diagrams.	(10M)
4.	a) b)	Explain different negative feedback topologies with neat diagrams. Draw the circuit diagram of a voltage shunt feedback using BJT and derive expression for voltage gain with feedback	(10M) (6M)
4. 5.		Draw the circuit diagram of a voltage shunt feedback using BJT and derive	. ,
	b)	Draw the circuit diagram of a voltage shunt feedback using BJT and derive expression for voltage gain with feedback Draw the RC phase shift oscillator circuit and derive the expression for frequency	(6M)
	b) a)	Draw the circuit diagram of a voltage shunt feedback using BJT and derive expression for voltage gain with feedbackDraw the RC phase shift oscillator circuit and derive the expression for frequency of oscillations.Explain the operation of Hartley Oscillator with neat diagram.Prove that in class A power amplifier if the distortion is 10% then the power	(6M) (8M)
5.	b)a)b)	Draw the circuit diagram of a voltage shunt feedback using BJT and derive expression for voltage gain with feedbackDraw the RC phase shift oscillator circuit and derive the expression for frequency of oscillations.Explain the operation of Hartley Oscillator with neat diagram.	(6M) (8M) (8M)
5.	 b) a) b) a) 	 Draw the circuit diagram of a voltage shunt feedback using BJT and derive expression for voltage gain with feedback Draw the RC phase shift oscillator circuit and derive the expression for frequency of oscillations. Explain the operation of Hartley Oscillator with neat diagram. Prove that in class A power amplifier if the distortion is 10% then the power given to the load increased by 1% A Class B push-pull amplifier has Vcc=50V, the collector voltage swing from Vcc down to 10V with input signal. If the transistor used as maximum power dissipation rating of 20W.Calculate i)The load presented by the output transformer ii)Power output iii) D.C power input iv)Efficiency of collector 	(6M) (8M) (8M) (8M)